Safe Parallel Programming with Session Java

Nicholas Ng*, Nobuko Yoshida* Olivier Pernet*, Raymond Hu*, and Yiannos Kryftis[†]

*Imperial College London [†]National Technical University of Athens

Abstract. The session-typed programming language Session Java (SJ) has proved to be an effective tool for distributed programming, promoting structured programming for communications and compile-time safety. This paper investigates the use of SJ for session-typed parallel programming, and introduces new language primitives for *chained iteration* and *multi-channel communication*. These primitives allow the efficient coordination of parallel computation across multiple processes, thus enabling SJ to express the complex communication topologies often used by parallel algorithms. We demonstrate that the new primitives yield clearer and safer code for pipeline, ring and mesh topologies through implementations of representative parallel algorithms. We then present a semantics and session typing system including the new primitives, and prove type soundness and deadlock-freedom for our implementations. The benchmark results show that the new SJ is substantially faster than the original SJ and performs competitively against MPJ Express¹ used as reference.

1 Introduction

The current practice of parallel and distributed programming is fraught with errors that go undetected until runtime, manifest themselves as deadlocks or communication errors, and often find their root in mismatched communication protocols. The Session Java programming language (SJ) [13] improves this status quo. SJ is an extension of Java with *session types*, supporting statically safe distributed programming by message-passing. Session types were introduced as a type system for the π -calculus [9, 22], and have been shown to integrate cleanly with formal models of object-oriented programming. The SJ compiler offers two strong static guarantees for session execution: (1) *communication safety*, meaning a session-typed process can never cause or encounter a communication error by sending or receiving unexpected messages; and (2) *deadlock-freedom* — a session-typed process will never block indefinitely on a message receive.

Parallel programs often make use of complex, high-level communication patterns such as globally synchronised iteration over chained topologies like rings and meshes. Yet modern implementations are still written using low-level languages and libraries, commonly C and MPI [14]: implementations make the best use of hardware, but at the cost of complicated programming where communication is entangled with computation. There is no global view of inter-process communication, and no formal guarantees are given about communication correctness, which often leads to hard-to-find errors.

¹ MPJ Express [17] is a Java implementation of the MPI standard. Extensive benchmarks comparing MPJ Express to other MPI implementations are presented in [17]. The benchmarks show performance competitive with C-based MPICH2.

We investigate parallel programming in SJ as a solution to these issues. However, SJ as presented in [13] only guarantees progress for each session in isolation: deadlocks can still arise from the interleaving of multiple sessions in a process. Moreover, implementing chained communication topologies without additional language support requires temporary sessions, opened and closed on every iteration — a source of nontrivial inefficiencies (see § 3 for an example). We need new constructs, well-integrated with existing binary sessions, to enable lightweight *global* communication safety and deadlock-freedom, increase expressiveness to support *structured programming for communication topologies* and improve *performance*.

Our new *multi-channel* session primitives fit these requirements, and make it possible to safely and efficiently express parallel algorithms in SJ. The combination of new primitives and a well-formed topology check extension to SJ compilation [13] bring the benefits of type-safe, structured communications programming to HPC. The primitives can be chained, yielding a simple mechanism for structuring global control flow. We formalise these primitives as novel extensions of the *session calculus*, and the correctness condition on the shape of programs enforced by a simple extension of SJ compilation. This allows us to prove *communication safety* and *deadlock-freedom*, and offers a new, lightweight alternative to multiparty session types for global type-safety.

Contributions. This paper constitutes the first introduction to parallel programming in SJ, in addition to presenting the following technical contributions:

- (§ 2) We introduce SJ as a programming language for type-safe, efficient parallel programming, including our implementation of *multi-channel* session primitives, and the extended SJ tool chain for parallel programming. We show that the new primitives enable clearer, more readable code.
- (§ 3) We discuss SJ implementations of parallel algorithms using the Jacobi solution to the discrete Poisson equation (§ 3) as an example. The algorithm uses communication topology representative of a large class of parallel algorithms, and demonstrates the practical use of our multi-channel primitives.
- (§ 4) We define the *multi-channel session calculus*, its operational semantics, and typing system. We prove that processes conforming to a *well-formed communication topology* (Definition 4.1) satisfy the subject reduction theorem (Theorem 4.1), which implies *type and communication-safety* (Theorem 4.2) and *deadlock-freedom* across multiple, interleaved sessions (Theorem 4.3).
- (§ 5) Performance evaluation of *n*-Body simulation and Jacobi solution algorithms, demonstrating the benefits of the new primitives. The SJ implementations using the new primitives show competitive performance against an MPJ Express [15].

Related and future work are discussed in $\S 6$. Detailed definitions, proofs, benchmark results and source code can be found at the on-line Appendix [6].

Acknowledgements. We thank the referees for their useful comments and Brittle Tsoi and Wayne Luk for their collaborations. This work is partially supported by EP-SRC EP/F003757 and EP/G015635.

2 Session-Typed Programming in SJ

This section firstly reviews the key concepts of session-typed programming using Session Java (SJ) [12, 13]. In (1), we outline the basic methodology; in (2), the protocol

structures supported by SJ. We then introduce the new session programming features developed in this paper to provide greater expressiveness and performance gains for *session-typed parallel programming*. In (3), we explain session *iteration chaining*; and in (4), the generalisation of this concept to the *multi-channel* primitives. Finally, (5) describes the *topology verification* for parallel programs.

(1) **Basic SJ programming.** SJ is an extension of Java for type-safe concurrent and distributed session programming. Session programming in SJ, as detailed in [13], starts with the declaration of the intended communication protocols as session types; we shall often use the terms *session type* and *protocol* interchangeably. A session is the interaction between two communicating parties, and its session type is written from the viewpoint of one side of the session. The following declares a protocol named P:

```
protocol P !<int>.?(Data)
```

Protocol P specifies that, at this side of the session, we first send (!) a message of Java type int, then receive (?) another message, an instance of the Java class Data, which finishes the session. After defining the protocol, the programmer implements the processes that will perform the specified communication actions using the SJ *session primitives*. The first line in the following code implements an Alice process conforming to the P protocol:

A: alice.send(42); Data d = (Data) alice.receive();//!<int>.?(Data) B: int i = bob.receiveInt(); bob.send(new Data()); //?(int).!<Data>

The alice variable refers to an object of class SJSocket, called a *session socket*, which represents one endpoint of an active session. The session-typed primitives for session-typed communication behaviour, such as send and receive, are performed on the session socket like method invocations. SJSocket declarations associate a protocol to the socket variable, and the SJ compiler statically checks that the socket is indeed used according to the protocol, ensuring the *correct communication behaviour* of the process.

This simple session application also requires a counterpart Bob process to interact with Alice. For safe session execution, the Alice and Bob processes need to perform matching communication operations: when Alice sends an int, Bob receives an int, and so on. Two processes performing matching operations have session types that are *dual* to each other. The dual protocol to P is protocol PDual ?(int).!<Data>, and a dual Bob process can be implemented as in the second line of the above listing.

(2) More complex protocol structures. Session types are not limited to sequences of basic message passing. Programmers can specify more complex protocols featuring *branching*, *iteration* and *recursion*.

The protocols and processes in Fig.1 demonstrate session iteration and branching. Process P1 communicates with P2 according to protocol IntAndBoolStream; P2 and P3 communicate following protocol IntStream. Like basic message passing, iteration and branching are coordinated by *active* and *passive* actions at each side of the session. Process P1 actively decides whether to continue the session iteration using outwhile (*condition*), and if so, selects a branch using outbranch(*label*). The former action implements the $![\tau]*$ type given by IntAndBoolStream, where τ is the $![Label1: \tau_1, Label2: \tau_2, \ldots]$ type implemented by the latter. Processes P2 and P3 passively follow

```
Р1
                                     P2
                                               Р3
protocol IntAndBoolStream ![!{Label1: !<int>, Label2: !<boolean>}]*
                           ?[?{Label1: ?<int>, Label2: ?(boolean)}]*
protocol IntAndBoolDual
                           ![!<int>]*
protocol IntStream
protocol IntStreamDual
                           ?[?(int)]*
                                        s2.outwhile(s1.inwhile()) {
P1: s.outwhile(x < 10) {</pre>
                                   P2:
                                          s1.inbranch() {
      s.outbranch(Label1) {
                                            case Label1:
        s.<u>send</u>(42);
                                              int i = s1.receiveInt();
    }}
                                              s2.send(i);
                                            case Label2:
P3: s.inwhile {
                                              boolean b = s1.receiveBool();
      int i = s.receiveInt();
                                              s2.send(42);
    3
                                        }}
```

Session socket s in P1 follows IntAndBoolStream, s1 and s2 in P2 follows IntAndBoolDual and IntStream, s in P3 follows IntStreamDual.

Fig. 1. Simple chaining of session iterations across multiple pipeline process.

the selected branch and the iteration decisions (received as internal control messages) using inbranch and inwhile, and proceed accordingly; the two dual protocols show the passive versions of the above iteration and branching types, denoted by ? in place of !.

So far, we have reviewed basic SJ programming features [13] derived from standard session type theory [9, 22]; the following paragraphs discuss new features motivated by the application of session types to parallel programming in practice.

(3) Expressiveness gains from iteration chaining. The three processes in Fig. 1 additionally illustrate session *iteration chaining*, forming a linear pipeline as depicted at the top of Fig. 1. The net effect is that *P1* controls the iteration of both its session with *P2* and transitively the session between *P2* and *P3*. This is achieved through the chaining construct s2.outwhile(s1.inwhile()) at *P2*, which receives the iteration decision from *P1* and forwards it to *P3*. The flow of both sessions is thus controlled by the same master decision from *P1*.

Iteration chaining offers greater expressiveness than the individual iteration primitives supported in standard session types. Normally, session typing for ordinary inwhile or outwhile loops must forbid operations on any session other than the session channel that of loop, to preserve linear usage of session channels. This means that e.g. s1.inwhile(){ s1.send(v); } is allowed, whereas s1.inwhile(){ s2.send(v); } is not. With the iteration chaining construct, we can now construct a process containing two interleaved inwhile or outwhile loops on separate sessions. In fact, session iteration chaining can be further generalised as we explain below.

(4) Multi-channel iteration primitives. Simple iteration chaining allows SJ programmers to combine multiple sessions into linear pipeline structures, a common pattern in parallel processing. In particular, type-safe session iteration (and branching) along a pipeline is a powerful benefit over traditional stream-based data flow [19]. More complex topologies, however, such as rings and meshes, require iteration signals to be di-



Fig. 2. Multi-channel iteration in a simple grid topology.

rectly forwarded from a given process to more than one other, and for multiple signals to be directed into a common sink; in SJ, this means we require the ability to send and receive multiple iteration signals over a set of session sockets. For this purpose, SJ introduces the generalised *multi-channel* primitives; the following focuses on multi-channel iteration, which extends the chaining constructs from above.

Fig. 2 demonstrates multi-channel iteration for a simple grid topology. Process *Master* controls the iteration on both the s1 and s2 session sockets under a single iteration condition. Processes *Forwarder1* and *Forwarder2* iterate following the signal from *Master* and forward the signal to *End*; thus, all four processes iterate in lock-step. Multi-channel inwhile, as performed by *End*, is intended for situations where multiple sessions are combined for iteration, but all are coordinated by an iteration signal from a common source; this means all the signals received from each socket of the inwhile will always agree — either to continue iterating, or to stop. In case this is not respected at run-time, the inwhile will throw an exception, resulting in session termination. Together, multi-channel primitives enable the type-safe implementation of parallel programming patterns like scatter-gather, producer-consumer, and more complex chained topologies. The basic session primitives express only disjoint behaviour within individual sessions, whereas the multi-channel primitives implement interaction across multiple sessions as a single, integrated structure.

(5) The SJ tool chain with topology verification. In previous work, the safety guarantees offered by the SJ compiler were limited to the scope of each independent binary (two-party) session. This means that, while any one session was guaranteed to be internally deadlockfree, this property may not hold in the presence of interleaved sessions in a process as a whole. The nodes in a parallel program typically make use of many interleaved sessions - with each of their neighbours in the chosen network topology. Furthermore, inwhile and outwhile in iteration chains must be correctly composed.

As a solution to this issue, we add a *topology verification* step to the SJ tool chain for parallel programs. Fig. 3 sum-



Fig. 3. The SJ tool chain.

marises the SJ tool chain for developing type-safe SJ parallel program on a distributed computing cluster. An SJ parallel program is written as a collection of SJ source files, where each file corresponds to a role in the topology. Topology verification (A) takes as input the source files and a *deployment configuration file*, listing the hosts where each process will be deployed and describing how to connect the processes. The sources and configuration files are then analysed statically to ensure the overall session topology of the parallel program conforms to a *well-formed topology* defined in Definition 4.1 in § 4, and in conjunction with session duality checks in SJ, precludes *global deadlocks* in parallel SJ programs (see Theorem 4.3). The source files are then compiled (B) to bytecode, and (C) deployed on the target cluster using details on the configuration file to instantiate and establish sessions with their assigned neighbours, ensuring the runtime topology is constructed according to the verified configuration file, and therefore safe execution of the parallel program.

3 Parallel Algorithms in SJ

This section presents the SJ implementation of a Jacobi method for solving the Discrete Poisson Equation and explains the benefits of the new multi-channel primitives. The example was chosen both as a representative real-world parallel programming application in SJ, and because it exemplifies a complex communication topology [8]. Implementations of other algorithms featuring other topologies, such as *n*-Body simulation (circular pipeline) and Linear Equation Solver (wraparound mesh), are available from [6].

Jacobi solution of the discrete Poisson equation: mesh topology. Poisson's equation is a partial differential equation widely used in physics and the natural sciences. Jacobi's algorithm can be implemented using various partitioning strategies. An early session-typed implementation [1] used a one-dimensional decomposition of the source matrix, resulting in a linear communication topology. The following demonstrates how the new multi-channel primitives are required to increase parallelism using a two-dimensional decomposition, i.e. using a 2D mesh communication topology. The mesh topology is used in a range of other parallel algorithms [3].

The discrete two-dimensional Poisson equation $(\nabla^2 u)_{ij}$ for a $m \times n$ grid reads:

$$u_{ij} = \frac{1}{4}(u_{i-1,j} + u_{i+1,j} + u_{i,j-1} + u_{i,j+1} - dx^2 g_{i,j})$$

where $2 \le i \le m-1$, $2 \le j \le n-1$, and dx = 1/(n+1). Jacobi's algorithm converges on a solution by repeatedly replacing each element of the matrix *u* by an adjusted average of its four neighbouring values and $dx^2g_{i,j}$. For this example, we set each $g_{i,j}$ to 0. Then, from the *k*-th approximation of *u*, the next iteration calculates:

$$u_{ij}^{k+1} = \frac{1}{4}(u_{i+1,j}^k + u_{i-1,j}^k + u_{i,j+1}^k + u_{i,j-1}^k)$$

Termination may be on reaching a target convergence threshold or on completing a certain number of iterations. Parallelisation of this algorithm exploits the fact that each element can be independently updated within each iteration. The decomposition divides the grid into subgrids, and each process will execute the algorithm for its assigned subgrid. To update the points along the boundaries of each subgrid, neighbouring processes need to exchange their boundary values at the beginning of each iteration.

```
protocol MasterToWorker
cbegin. // Open a session with the Worker
!<int>.!<int>. // Send matrix dimensions
![ // Main loop: checking convergence condition
!<double[]>. // Send our boundary values...
?(double[]). // ..and receive our neighbour's
?(ConvergenceValues) // Convergence data for neighbouring subgrid
]* // (end of main loop)
```

Fig. 4. The session type between the Master and Workers for the Jacobi algorithm.

A 2D mesh implementation is shown in Fig. 7. The *Master* node controls iteration from the top-left corner. Nodes in the centre of the mesh receive iteration control signals from their top and left neighbours, and propagate them to the bottom and right. Nodes at the edges only propagate iteration signals to the bottom or the right, and the final node at the bottom right only receives signals and does not propagate them further.

The session type for communication from the *Master* to either of the *Workers* under it or at its right is given in Fig. 4. The *Worker's* protocol for interacting with the *Master* is the dual of *MasterToWorker*; the same protocol is used for interaction with other *Workers* at their right and bottom (except for *Workers* at the edges of the mesh).

As listed in Fig. 5, it is possible to express the complex 2D mesh using singlechannel primitives only. However, this implementation suffers from a problem: without the multi-channel primitives, there is no way of sending iteration control signals both horizontally and vertically; the only option is to open and close a temporary session in every iteration (Fig. 7), an inefficient and counter-intuitive solution. Moreover, the continuous nature of the vertical iteration sessions cannot be expressed naturally.

Having noted this weakness, Fig. 6 lists a revised implementation, taking advantage of multi-channel inwhile and outwhile. The multi-channel inwhile allows each *Worker* to receive iteration signals from the two processes at its top and left. Multichannel outwhile lets a process control both processes at the right and bottom. Together, these two primitives completely eliminate the need for repeated opening and closing of intermediary sessions in the single-channel version. The resulting implementation is clearer and also much faster. See § 5 for the benchmark results.

4 Multi-channel Session π -Calculus

This section formalises the new nested iterations and multi-channel communication primitives and proves correctness of our implementation. Our proof method consists of:

- 1. We first define programs (*i.e.* starting processes) including the new primitives, and then define operational semantics with running processes modelling intermediate session communications.
- 2. We define a typing system for programs and running processes.
- 3. We prove that if a group of running processes conforms to a *well-formed topology*, then they satisfy the subject reduction theorem (Theorem 4.1) which implies type and communication-safety (Theorem 4.2) and deadlock-freedom (Theorem 4.3).
- 4. Since programs for our chosen parallel algorithms conform to a well-formed topology, we conclude that they satisfy the above three properties.

```
Master:
right.outwhile(notConverged()) {
 under = chanUnder.request();
 sndBoundaryVal(right, under);
 rcvBoundaryVal(right, under);
 doComputation(rcvRight, rcvUnder);
 rcvConvergenceVal(right, under);
}
Worker :
right.outwhile(left.inwhile) {
 over = chanOver.accept();
 under = chanUnder.request();
 sndBoundaryVal(left,right,over,
      under);
 rcvBoundaryVal(left,right,over,
      under);
 doComputation(rcvLeft,rcvRight,
      rcvOver,rcvUnder);
 sndConvergenceVal(left,top);
}
WorkerSE :
left.inwhile {
 over = chanOver.request();
 sndBoundaryVal(left,over);
 rcvBoundaryVal(left,over);
 doComputation(rcvLeft,rcvOver);
 sndConvergenceVal(left,top);
}
```

```
Master:
<under,right>.outwhile(
    notConverged()) {
 sndBoundaryVal(right, under);
 rcvBoundaryVal(right, under);
  doComputation(rcvRight, rcvUnder
      );
 rcvConvergenceVal(right, under);
}
Worker :
<under,right>.outwhile
    (<over,left>.inwhile) {
  sndBoundaryVal(left,right,over,
      under);
 rcvBoundaryVal(left,right,over,
      under);
 doComputation(rcvLeft,rcvRight,
      rcvOver,rcvUnder);
 sndConvergenceVal(left,top);
}
WorkerSE :
<over,left>.inwhile {
 sndBoundaryVal(left,over);
 rcvBoundaryVal(left,over);
 doComputation(rcvLeft,rcvOver);
 sndConvergenceVal(left,top);
}
```

Fig. 6. Efficient 2D mesh implementation using multi-outwhile and multi-inwhile.



Fig. 7. Initial and improved communication patterns in the 2D mesh implementation.

single-channel primitives only.



4.1 Syntax

The session π -calculus we treat extends [9]. Fig. 8 defines its syntax. Channels (u, u', ...) can be either of two sorts: *shared channels* (a, b, x, y) or *session channels* (k, k', ...). Shared channels are used to open a new session. In accepting and requesting processes, the name *a* represents the public interaction point over which a session may commence. The bound variable *k* represents the actual channel over which the session communications will take place. Constants (c, c', ...) and expressions (e, e', ...) of ground types (booleans and integers) are also added to model data. *Selection* chooses an available branch, and *branching* offers alternative interaction patterns; *channel send* and *channel receive* enable session delegation [9]. The *sequencing*, written *P*; *Q*, meaning that *P* is executed before *Q*. This syntax allows for complex forms of synchronisation, joining, and forking since *P* can include any parallel composition of arbitrary processes. The second addition is that of *multicast inwhile* and *outwhile*, following SJ syntax. Note that the definition of expressions includes multicast inwhile $\langle k_1 \dots k_n \rangle$.inwhile, in order to allow inwhile as an outwhile loop condition. The control message $k \ddagger [b]$ created by outwhile appears only at runtime.

The precedence of the process-building operators is (from the strongest) " $\triangleleft, \triangleright, \{\}$ ", ".", ";" and "|". Moreover we define that "." associates to the right. The binders for channels and variables are standard.

(Values)	(E>	(pressions)		
<pre>v ::= a,b,x,y sha</pre>	red names boolean integer		$e \mid \texttt{not}(e) \dots \\ k_n angle.$ inwhile	value, sum, not inwhile
(Processes)	(Prefixed proc	cesses)		
P ::= 0 inaction	n $T ::= \overline{a}(k).P$	request	def D in P	recursion
T prefixed		accept	$ k \triangleleft l$	selection
P; Q sequence	i $ \overline{k} \langle e \rangle$	sending	$ k \triangleright \{l_1 : P_1[] \cdots [] l_n$	$: P_n \}$ branch
P Q paralle	k(x).P	reception	if <i>e</i> then <i>P</i> else	Q conditional
(vu)P hiding	g $ k \langle k' \rangle$	sending	$ \langle k_1 \dots k_n \rangle$.inwhil	$e\{Q\}$ inwhile
(Declaration)	$\mid k(k').F$	P reception	$ \langle k_1 \dots k_n \rangle$.outwhi	$le(e){P}$ outwhile
D ::= X(xk) = P	$\mid X[ek]$	variables	$ k \dagger [b]$	message

Fig. 8. Syntax.

We formalise the reduction relation \longrightarrow in Fig.8 up to the standard structural equivalence \equiv with the rule **0**; $P \equiv P$ based on [9]. Reduction uses the standard *evaluation contexts* defined as:

$$\begin{array}{l|l} E ::= [] \mid E; P \mid E \mid P \mid (vu)E \mid \text{def } D \text{ in } E \\ \mid \text{ if } E \text{ then } P \text{ else } Q \mid \langle k_1 \dots k_n \rangle. \text{outwhile}(E) \{P\} \mid E + e \mid \cdots \end{array}$$

We use the notation $\prod_{i \in \{1...n\}} P_i$ to denote the parallel composition of $(P_1 \mid \cdots \mid P_n)$.

Rules [LINK] is a session initiation rule where a fresh channel k is created, then restricted because the leading parts now share the channel k to start private interactions. Rule [COM] sends data. Rule [LBL] selects the *i*-th branch, and rule [PASS] passes a session channel k for delegation. The standard conditional and recursive agent rules [IF1], [IF2] and [DEF] originate in [9].

Rule [Iw1] synchronises with n asynchronous messages if they all carry true. In this

$$\begin{split} a(k).P_1 \mid \overline{a}(k).P_2 &\longrightarrow (\forall k)(P_1 \mid P_2) & \overline{k}\langle c \rangle \mid k(x).P_2 &\longrightarrow P_2\{c/x\} \quad \text{[LINK],[COM]} \\ k \triangleright \{l_1:P_1[] \cdots []l_n:P_n\} \mid k \triangleleft l_i \longrightarrow P_i \quad (1 \leq i \leq n) & k\langle k' \rangle \mid k(k').P_2 \longrightarrow P_2 \quad \text{[LBL],[PASS]} \\ & \text{if true then } P \text{ else } Q \longrightarrow P \quad \text{if false then } P \text{ else } Q \longrightarrow Q \quad \text{[IF]} \\ & def X(xk) = P \text{ in } X[ck] \longrightarrow def X(xk) = P \text{ in } P\{c/x\} \quad \text{[DEF]} \\ & \langle k_1 \dots k_n \rangle.\text{inwhile}\{P\} \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{true}] \longrightarrow P; \langle k_1 \dots k_n \rangle.\text{inwhile}\{P\} \quad |\Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow \mathbf{0} \quad \text{[IW2]} \\ & E[\langle k_1 \dots k_n \rangle.\text{inwhile}\} \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow E[\text{true}] \quad \text{[IWE1]} \\ & E[\langle k_1 \dots k_n \rangle.\text{inwhile}\} \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow E[\text{false}] \quad \text{[IWE2]} \\ & E[\langle k_1 \dots k_n \rangle.\text{inwhile}] \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow E[\text{false}] \quad \text{[IWE2]} \\ & E[\langle k_1 \dots k_n \rangle.\text{inwhile}] \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow E[\text{false}] \quad \text{[IWE2]} \\ & E[\langle k_1 \dots k_n \rangle.\text{inwhile}] \mid \Pi_{i \in \{1..n\}}k_i \dagger [\text{false}] \longrightarrow E[\text{false}] \quad \text{[IWE2]} \\ & E[\langle k_1 \dots k_n \rangle.\text{outwhile}(e)\{P\}] \longrightarrow E'[P; \langle k_1 \dots k_n \rangle.\text{outwhile}(e)\{P\}] \\ & \quad |\Pi_{i \in \{1..n\}}k_i \dagger [\text{fune}] \quad \text{[Ow1]} \end{aligned}$$

$$E[e] \longrightarrow^{*} E'[\texttt{false}] \Rightarrow$$

$$E[\langle k_1 \dots k_n \rangle, \texttt{outwhile}(e) \{P\}] \longrightarrow E'[\mathbf{0}] \mid \prod_{i \in \{1, n\}} k_i \dagger [\texttt{false}] \qquad [Ow2]$$

$$P \equiv P' \text{ and } P' \longrightarrow Q' \text{ and } Q' \equiv Q \implies P \longrightarrow Q$$
 [STR]

$$e \longrightarrow e' \Rightarrow E[e] \longrightarrow E[e'] \qquad P \longrightarrow P' \Rightarrow E[P] \longrightarrow E[P']$$
$$P \mid Q \longrightarrow P' \mid Q' \Rightarrow E[P] \mid Q \longrightarrow E[P'] \mid Q' \qquad \text{(EVAL)}$$

In [Ow1] and [Ow2], we assume $E = E' \mid \prod_{i \in \{1...n\}} k_i \dagger [b_i]$

Fig. 9. Reduction rules.

case, it repeats again. Rule [Iw2] is its dual and synchronises with *n* false messages. In this case, it moves to the next command. On the other hand, if the results are mixed (i.e. b_i is true, while b_j is false), then it is stuck. In SJ, it will raise the exception, cf. § 2 (4). The rules for expressions are defined similarly. The rules for outwhile generates appropriate messages. Note that the assumption $E[e] \longrightarrow E'[\texttt{true}]$ or $E[e] \longrightarrow E'[\texttt{false}]$ is needed to handle the case where *e* is an inwhile expression.

In order for our reduction rules to reflect SJ's actual behaviour, inwhile rules should have precedence over outwhile rules. Note that our algorithms do not cause an infinite generation of $k \dagger [b]$ by outwhile: this is ensured by the well-formed topology criteria described later, together with this priority rule.

4.2 Types, Typing System and Well-Formed Topologies

This subsection presents types and typing systems. The key point is an introduction of types and typing systems for asynchronous runtime messages. We then define the notation of a well-formed topology.

Types. The syntax of types, an extension of [9], follows:

Sort	S	$::=$ nat $ $ bool $ $ \langle	$\langle \alpha, \overline{lpha} angle$			
Partial session	τ	$::= \varepsilon \mid \tau; \tau \mid ?[S]$	$?[\alpha] \& \{l_1: \tau_1, \ldots, l_n: \tau_n\} ![\tau]^* \mathbf{x}$			
		![S]	$![\alpha] \oplus \{l_1: \tau_1, \ldots, l_n: \tau_n\} ?[\tau]^* \mu \mathbf{x}.\tau$			
Completed session	α	$::= au.$ end \mid ot	Runtime session $\beta ::= \alpha \mid \alpha^{\dagger} \mid \dagger$			
Sorts include a pair type for a shared channel and base types. The partial session type						

Sorts include a pair type for a shared channel and base types. The partial session type τ represents intermediate sessions. ε represents inaction and τ ; τ is a sequential composition. The rest is from [9]. The types with ! and ? express respectively the sending

and reception of a value *S* or session channel. The selection type \oplus represents the transmission of the label l_i followed by the communications described by τ_i . The branching type & represents the reception of a label l_i chosen in the set $\{l_1, \ldots, l_n\}$ followed by the communications described by τ_i . Types $![\tau]^*$ and $?[\tau]^*$ are types for outwhile and inwhile. The types are considered up to the equivalence: $\&\{l_1: \tau_1, \ldots, l_n: \tau_n\}$.end $\equiv \&\{l_1: \tau_1..., l_n: \tau_n..., l_n: \tau_n\}$. This equivalence ensures all partial types $\tau_1 \ldots \tau_n$ of selection ends, and are compatible with each other in the completed session type (and vice versa). ε is an empty type, and it is defined so that $\varepsilon; \tau \equiv \tau$ and $\tau; \varepsilon \equiv \tau$.

Runtime session syntax represents partial composed runtime message types. α^{\dagger} represents the situation inwhile or outwhile are composed with messages; and \dagger is a type of messages. The meaning will be clearer when we define the parallel composition.

Judgements and environments. The typing judgements for expressions and processes are of the shape:

$$\Gamma; \Delta \vdash e \triangleright S$$
 and $\Gamma \vdash P \triangleright \Delta$

where we define the environments as $\Gamma ::= \emptyset | \Gamma \cdot x : S | \Gamma \cdot X : S\alpha$ and $\Delta ::= \emptyset | \Delta \cdot k : \beta$. Γ is the *standard environment* which associates a name to a sort and a process variable to a sort and a session type. Δ is the *session environment* which associates session channels to running session types, which represents the open communication protocols. We often omit Δ or Γ from the judgement if it is empty.

Sequential and parallel compositions of environments are defined as:

 $\begin{array}{l} \Delta;\Delta'=\Delta\backslash\operatorname{dom}(\Delta')\cup\Delta'\backslash\operatorname{dom}(\Delta)\cup\{k\colon\Delta(k)\backslash\operatorname{end};\Delta'(k)\mid k\in\operatorname{dom}(\Delta)\cap\operatorname{dom}(\Delta')\}\\ \Delta\circ\Delta'=\Delta\backslash\operatorname{dom}(\Delta')\cup\Delta'\backslash\operatorname{dom}(\Delta)\cup\{k\colon\Delta(k)\circ\Delta'(k)\mid k\in\operatorname{dom}(\Delta)\cap\operatorname{dom}(\Delta')\}\\ \text{where }\Delta(k)\backslash\operatorname{end}\text{ means we delete end from the tail of the types (e.g. <math>\tau.\operatorname{end}\backslash\operatorname{end}=\tau$). Then the resulting sequential composition is always well-defined. The parallel composition of the environments must be extended with new running message types. Hence $\beta\circ\beta'$ is defined as either (1) $\alpha\circ\overline{\alpha}=\bot$; (2) $\alpha\circ\dagger=\alpha^{\dagger}$ or (3) $\alpha\circ\overline{\alpha}^{\dagger}=\bot^{\dagger}$. Otherwise the composition is undefined. Here $\overline{\alpha}$ denotes a dual of α (defined by exchanging ! to ? and & to \oplus ; and vice versa). (1) is the standard rule from session type algebra, which means once a pair of dual types are composed, then we cannot compose any processes with the same channel further. (2) means a composition of an iteration of type α and *n*-messages of type \dagger becomes α^{\dagger} . This is further composed with the dual $\overline{\alpha}$ by (3) to complete a composition. Note that \bot^{\dagger} is different from \bot since \bot^{\dagger} represents a situation that messages are not consumed with inwhile yet.

Typing rules. We explain the key typing rules for the new primitives (Fig. 10). Other rules are similar with [9] and left to [6].

[EINWHILE] is a rule for inwhile-expression. The iteration session type of k_i is recorded in Δ . This information is used to type the nested iteration with outwhile in rule [OUTWHILE]. Rule [INWHILE] is dual to [OUTWHILE]. Rule [MESSAGE] types runtime messages as \dagger . Sequential and parallel compositions use the above algebras to ensure the linearity of channels. **Well-formed topologies.** We now define the well-formed topologies. Since our multichannel primitives offer an effective, structured message passing synchronisation mechanism, the following simple definition is sufficient to capture deadlock-freedom in representative topologies for parallel algorithms. Common topologies in parallel algorithms such as circular pipeline, mesh and wraparound mesh all conform to our well-

$$\begin{array}{ll} \displaystyle \frac{\Delta = k_1 \colon ?[\tau_1]^*.\mathrm{end},...,k_n \colon ?[\tau_n]^*.\mathrm{end}}{\Gamma;\Delta \vdash \langle k_1 \ldots k_n \rangle.\mathrm{inwhile} \triangleright \mathrm{bool}} & \frac{\Gamma \vdash b \triangleright \mathrm{bool}}{\Gamma \vdash k \dagger [b] \triangleright k \colon \dagger} & \text{[EINWHILE],[MESSAGE]} \\ \\ \displaystyle \frac{\Gamma;\Delta \vdash e \triangleright \mathrm{bool}}{\Gamma \vdash P \triangleright \Delta \cdot k_1 \colon \tau_1.\mathrm{end} \cdots k_n \colon \tau_n.\mathrm{end}} & \text{[OUTWHILE]} \\ \\ \displaystyle \frac{\Gamma \vdash Q \triangleright \Delta \cdot k_1 \colon \tau_1.\mathrm{end} \cdots k_n \colon \tau_n.\mathrm{end}}{\Gamma \vdash \langle k_1 \ldots k_n \rangle.\mathrm{inwhile} \{Q\} \triangleright \Delta \cdot k_1 \colon ?[\tau_1]^*.\mathrm{end},...,k_n \colon ?[\tau_n]^*.\mathrm{end}} & \text{[INWHILE]} \\ \\ \displaystyle \frac{\Gamma \vdash P \triangleright \Delta \cdot k_1 \colon \tau_1.\mathrm{end} \cdots k_n \colon \tau_n.\mathrm{end}}{\Gamma \vdash \langle k_1 \ldots k_n \rangle.\mathrm{inwhile} \{Q\} \triangleright \Delta \cdot k_1 \colon ?[\tau_1]^*.\mathrm{end},...,k_n \colon ?[\tau_n]^*.\mathrm{end}} & \text{[INWHILE]} \\ \\ \displaystyle \frac{\Gamma \vdash P \triangleright \Delta \quad \Gamma \vdash Q \triangleright \Delta'}{\Gamma \vdash P; \ Q \triangleright \Delta; \Delta'} & \frac{\Gamma \vdash P \triangleright \Delta \quad \Gamma \vdash Q \triangleright \Delta'}{\Gamma \vdash P \mid Q \triangleright \Delta \circ \Delta'} & \text{[Seq],[Conc]} \end{array} \end{array}$$

Fig. 10. Key typing rules

formed topology definition below [6]. Below we call *P* is a *base* if *P* is either **0**, $\overline{k}\langle e \rangle$, k(x).**0**, $k \triangleleft l$ or $k \triangleright \{l_1 : \mathbf{0}[] \cdots []l_n : \mathbf{0}\}$.

Definition 4.1 (Well-formed topology.). Suppose a group of *n* parallel composed processes $P = P_1 \mid \ldots \mid P_n$ such that $\Gamma \vdash P \triangleright \Delta$ with $\Delta(k) = \bot$ for all $k \in \text{dom}(\Delta)$; and $k_{(i,j)}$ denotes a free session channel from P_i to P_j . We say *P* conforms to a *well-formed topology* if *P* inductively satisfies one of the following conditions:

- 1. (inwhile and outwhile)
 - $$\begin{split} & P_1 = \langle \vec{k}_1 \rangle.\texttt{outwhile}(e) \{Q_1\} \ P_i = \langle \vec{k}_i \rangle.\texttt{outwhile}(\langle \vec{k}'_i \rangle.\texttt{inwhile}) \{Q_i\} \ (2 \leq i < n) \\ & P_n = \langle \vec{k}'_n \rangle.\texttt{inwhile}\{Q_n\} \qquad \vec{k}_i \subset k_{(i,i+1)} \cdots k_{(i,n)}, \vec{k}'_i \subset k_{(1,i)} \cdots k_{(i-1,i)} \\ & \text{and} \ (Q_1 \mid \cdots \mid Q_n) \ \texttt{conforms to a well-formed topology.} \end{split}$$
- 2. (sequencing) $P_i = Q_{1i}; ...; Q_{mi}$ where $(Q_{j1} | Q_{j2} | \cdots | Q_{jn})$ conforms to a well-formed topology for each $1 \le j \le m$.
- 3. (base) (1) session actions in P_i follow the order of the index (e.g. the session actions at $k_{(i,j)}$ happens before $k_{(h,g)}$ if (i,j) < (h,g)), then the rest is a base process P'_i ; and (2) P_i includes neither shared session channels, inwhile nor outwhile.

The figure below explains condition (1) of the above definition, ensuring consistency of control flows within iterations. Subprocesses P_i are ordered by their process index *i*. A process P_i can only send outwhile control messages to processes with a higher index via \vec{k}_i (denoted by $k_{(i,m)}$), while it can receive messages from those with a lower index via \vec{k}'_i (denoted by $k_{(h,i)}$). This ordering guarantees absence of cycles of communications.



There is only one source P_1 (only sends outwhile control messages) and one sink P_n (only receives those messages). (2) says that a sequential composition of well-formed topologies is again well-

formed. (3) defines base cases which are commonly found in the algorithms: (3-1) means that since the order of session actions in P_i follow the order of the indices, $\Pi_i P_i$ reduces to $\Pi_i P'_i$ without deadlock; then since $\Pi_i P'_i$ is a parallel composition of base processes where each channel *k* has type \perp , $\Pi_i P'_i$ reduces to **0** without deadlock. (3-2) ensures a single global topology.

4.3 Subject Reduction, Communication Safety and Deadlock Freedom

We state here that process groups conforming to a well-formed topology satisfy the main theorems. The full proofs can be found in [6].

Theorem 4.1 (Subject reduction) Assume *P* forms a well-formed topology and $\Gamma \vdash P \triangleright \Delta$. Suppose $P \longrightarrow^* P'$. Then we have $\Gamma \vdash P' \triangleright \Delta'$ with for all k (1) $\Delta(k) = \alpha$ implies $\Delta'(k) = \alpha^{\dagger}$; (2) $\Delta(k) = \alpha^{\dagger}$ implies $\Delta'(k) = \alpha$; or (3) $\Delta(k) = \beta$ implies $\Delta'(k) = \beta$.

(1) and (2) state an intermediate stage where messages are floating; or (3) the type is unchanged during the reduction. The proof requires to formulate the intermediate processes with messages which are started from a well-formed topology, and prove they satisfy the above theorem.

We say process has a *type error* if expressions in P contains either a type error of values or constants in the standard sense (e.g. if 100 then P else Q).

To formalise communication safety, we need the following notions. Write inwhile(Q) for either inwhile or inwhile {Q}. We say that a processes P is a *head subprocess* of a process Q if $Q \equiv E[P]$ for some evaluation context E. Then *k*-process is a head process prefixed by subject k (such as $\bar{k}\langle e \rangle$). Next, a *k*-redex is the parallel composition of a pair of k-processes. i.e. either of form of a pair such that $(\bar{k}\langle e \rangle, k(x).Q), (k < l, k > \{l_1 : Q_1[] \cdots []l_n : Q_n\}), (\bar{k}\langle k' \rangle, k(k').P), (\langle k_1 \dots k_n \rangle.outwhile(e) \{P\}, \langle k'_1 \dots k'_m \rangle.inwhile(Q))$ with $k \in \{k_1, \dots, k_n\} \cap \{k'_1, \dots, k'_m\}$ or $(k \dagger [b] | \langle k'_1 \dots k'_m \rangle.inwhile(Q))$ with $k \in \{k_1, \dots, k_n\}$. Then P is a communication error if $P \equiv (v\tilde{u})(\det D \text{ in } (Q | R))$ where Q is, for some k, the parallel composition of two or more k-processes that do not form a k-redex. The following theorem is direct from the subject reduction theorem [22, Theorem 2.11].

Theorem 4.2 (Type and communication safety) A typable process which forms a well-formed topology never reduces to a type nor communication error.

Below we say *P* is *deadlock-free* if for all *P'* such that $P \longrightarrow^* P'$, $P' \longrightarrow$ or $P' \equiv \mathbf{0}$. The following theorem shows that a group of typable multiparty processes which form a well-formed topology can always move or become the null process.

Theorem 4.3 (Deadlock-freedom) Assume P forms a well-formed topology and $\Gamma \vdash P \triangleright \Delta$. Then P is deadlock-free.

Now we reason Jacobi algorithm in Fig. 6. We only show the master P_1 and the worker in the middle P_5 (the indices follow the right picture of Fig. 7).

$$\begin{array}{l} P_{1} = \langle k_{(1,2)}, k_{(1,4)} \rangle. \texttt{outwhile}(e) \{ \overline{k_{(1,2)}} \langle \texttt{d}[] \rangle; k_{(1,2)}(x). \overline{k_{(1,4)}} \langle \texttt{d}[] \rangle; k_{(1,4)}(y). \textbf{0} \} \\ P_{5} = \langle k_{(5,7)}, k_{(5,8)} \rangle. \texttt{outwhile}(\langle k'_{(2,5)}, k'_{(3,5)} \rangle. \texttt{inwhile}) \{ k'_{(2,5)}(w). \overline{k'_{(2,5)}} \langle \texttt{d}[] \rangle; k'_{(3,5)}(x). \overline{k'_{(3,5)}} \langle \texttt{d}[] \rangle; \overline{k_{(5,7)}} \langle \texttt{d}[] \rangle; k_{(5,7)}(y). \overline{k_{(5,8)}} \langle \texttt{d}[] \rangle; k_{(5,8)}(z). \textbf{0} \} \\ \end{array}$$

where d[] denotes the type of array with double. We can easily prove they are typable and forms the well-formed topology satisfying the conditions (1) and (3) in Definition 4.1. Hence it is type and communication-safe and deadlock-free. [6] lists the full definition and more complex algorithms which conform to a well-formed topology.



Fig. 11. SJ with and without multi-channel primitives and MPJ Express (left: 3-nodes *n*-Body simulation, right: 9-nodes Jacobi solution)

5 Performance Evaluation

This section presents performance results for several implementations of the *n*-Body simulation (details in [6, § A.1]), and Jacobi solution presented in § 3. We evaluated our implementations on a 9-node cluster for our benchmark, and each of the points is an average of 4 runs of the benchmark. All of them comprise an AMD PhenomX4 9650 2.30GHz CPU with 8GB RAM. The main objectives of these benchmarks is (1) to investigate the benefits of the new multi-channel primitives, comparing Old SJ (without the new primitives) and Multi-channel SJ (with the new primitives); and (2) compare those with MPJ Express [15] for reference. Fig. 11 shows a clear improvement when using the new multi-channel primitives in SJ. Multi-channel SJ also performs competitively against MPJ Express in both benchmarks. Hence SJ can be a viable alternative to MPI programming in Java, with the additional assurances of communication-safety and deadlock-free.

6 Related and Future Work

Due to space limitations, we focus on comparisons with extensions of Java with session types and MPI. Other related work, including functional languages with session types as well as HPC and PGAS languages can be found in the full version [6].

Implementations of session types in Java. SJ was introduced in [13] as the first general-purpose session-typed distributed programming language. Another recent extension of SJ added event-based programming primitives [12], for a different target domain: scalable and type-safe event-driven implementation of applications that feature a large number of concurrent but independent threads (e.g. Web servers). The preliminary experiments with parallel algorithms in SJ were reported in a workshop paper [1]. This early work considered only simple iteration chaining without analysis of deadlock-freedom, and without the general multi-channel primitives required for efficient representation of the complex topologies tackled here. The present paper also presents the formal semantics, type system, and proofs for type soundness and deadlock-freedom for the new primitives, which have not been studied in [1].

The Bica language [7] is an extension of Java also implementing binary sessions, which focuses on allowing session channels to be used as fields in classes. Bica does not support multi-channel primitives and does not guarantee deadlock-freedom across multiple sessions. See [11, 12] for more comparisons with [7]. A recent work [18] extends SJ-like primitives with multiparty session types and studies type-directed optimisations for the extended language. Their design is targeted at more loosely-coupled distributed applications than parallel algorithms, where processes are tightly-coupled and typically communicate via high-bandwidth, low-latency media; their optimisations, such as message batching, could increase latency and lower performance. It does not support features such as session delegation, session thread programming and transport independence [11, § 4.2.3], which are integrated into SJ. The latter in particular, together with SJ alias typing [11, § 3.1] (for session linearity), offers transparent portability of SJ parallel algorithm code over TCP and shared memory with zero-copy optimisations.

Message-based parallel programming. The present paper focuses on language and typing support for communications programming, rather than introducing a supplementary API. In comparison to the standard MPI libraries [8, §4], SJ offers structured communication programming from the natural abstraction of typed sessions and the associated static assurance of type and protocol safety. Recent work [20] applies modelchecking techniques to standard MPI C source code to ensure correct matching of sends and receives using a pre-existing test suite. Their verifier, ISP, exploits independence between thread actions to reduce the state space of possible thread interleavings of an execution, and checks for deadlocks in the remaining states. In contrast, our session type-based approach does not depend on external testing, and a valid, compiled program is guaranteed communication-safe and deadlock-free in a matter of seconds. SJ thus offers a performance edge even in the cases of complex interactions (cf. [6]). The MPI API remains low-level, easily leading to synchronisation errors, message type errors and deadlocks [8]. From our experiences, programming message-based parallel algorithms with SJ are much easier than programming based on MPI functions, which, beside lacking type checking for protocol and communication safety, often requires manipulating numerical process identifiers and array indexes (e.g. for message lengths in the *n*-Body program) in tricky ways. Our approach gives a clear definition of a class of communication-safe and deadlock-free programs as proved in Theorems 4.2 and 4.3, which have been statically checked without exploring all execution states for all possible thread interleavings. Finally, benchmark results in $\S5$ demonstrate how SJ programs can deliver the above benefits and perform competitively against a Java-based MPI [15].

Future work. Our previous work [21] shows type-checking for parallel algorithms based on parameterised multiparty sessions requires type equality, so type checking is undecidable in the general case. The method developed in this paper is not only decidable, but also effective in practice as we can reuse the existing binary SJ language, type-checker and runtime, with extensions to the new multi-channel inwhile and outwhile primitives for structuring message-passing communications and iterations. To validate more general communication topologies beyond the well-formed condition and typical parallel algorithms, we plan to incorporate new primitives into multiparty session types [10, 18] by extending the end-point projection algorithm based on roles [4]. Preliminary results from a manual SJ-to-C translation have shown large performance

gains for FPGA implementations [16]. Future implementation efforts will include a natively compiled, C-like language targeted at low overheads and efficiency for HPC and systems programming. We also plan to incorporate recent, unimplemented theoretical advances, including logical reasoning [2] to prove the correctness of parallel algorithms.

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A Appendix to Section 3

We present here an additional parallel algorithm implementation in SJ, which further demonstrates the benefits of our multi-channel primitives.

A.1 *n*-Body simulation: Ring Topology

The parallel *n*-Body algorithm organises the constituent processes into a circular pipeline, an example of the ring communication topology. The ring topology is used by many other parallel algorithms, like matrix multiplication and LU matrix decomposition [3].

The *n*-Body problem involves finding the motion, according to classical mechanics, of a system of particles given their masses and initial positions and velocities. Parallelisation of the simulation algorithm is achieved by dividing the particle set among a set of *m* worker processes. The idea is that each simulation step involves a series of inner steps, which perform a running computation while forwarding the data from each process around the ring one hop at a time; after m - 1 inner steps, each process has seen all the data from every other, and the current simulation step is complete.

The following session type describes the communication protocol of our implementation. This is the session type for a *Worker*'s interaction with its left neighbour.

```
protocol WorkerToLeft
sbegin. // Accept session request from left neighbour
!<int>. // Forward init counter to determine number of processes
?[ // Main loop (loop controlled by left neighbour)
?[ // Pipeline stages within each simulation step
!<Particle[]> // Pass current particle state along the ring
]* ]*
```

The interaction with the right neighbour follows the dual protocol. The *WorkerLast* and *Master* nodes follow slightly different protocols, in order to close the ring structure and bootstrap the pipeline interaction.

In the SJ implementation, each node establishes two sessions with the left and right neighbours, and the iteration of every session in the pipeline is centrally controlled by the *Master* node. Without the multi-channel iteration primitives, there is no adequate way of closing the ring (sending data from the *WorkerLast* node to the *Master*); the only option is to open and close a temporary session with each iteration (Fig. 12) [1], an inefficient and counter-intuitive solution, as depicted on the left in Fig. 14 (the loosely dashed line indicates the temporary connection).

By contrast, Fig.13 gives the implementation of the ring topology using a multioutwhile at the *Master* node, and a multi-inwhile at *WorkerLast*. Data is still passed left-to-right, but the final iteration control link (the bold arrow on the right in Fig. 14) is reversed. This allows the *Master* to create the final link just once (at the start of the algorithm) like the other links, and gives the *Master* full control over the whole pipeline.

The full process definition of an *n*-Body simulation is listed as follows

A.2 Linear Equation Solver: Wraparound Mesh Topology

Linear equations are at the core of many engineering problems. Solving a system of linear equations consists in finding x such that Ax = b, where A is an $n \times n$ matrix and x and b are vectors of length n.

A whole range of methods for solving linear systems are available. One of the most amenable to parallelisation is the Jacobi method. It is based on the observation that the matrix *A* can be decomposed into a diagonal component and a remainder: A = D + R. The equation Ax = b is then equivalent to $x = D^{-1}(b - Rx)$, again equivalent to finding the solution to the *n* equations $\sum_{j=1}^{n} \alpha_{ij}x_j = b_i$ for i = 1, ..., n. Solving the *i*-th equation for x_i yields: $x_i = \frac{1}{\alpha_{ii}}(b_i - \sum_{j \neq i} \alpha_{ij}x_j)$, which suggests the iterative method: $x_i^{(k+1)} = \frac{1}{a_{ii}}(b_i - \sum_{j \neq i} \alpha_{ij}x_j^{(k)})$, where $k \ge 0$ and $x^{(0)}$ is an initial guess at the solution vector. The algorithm iterates until the normalised difference between successive iterations is less than some predefined error.

Our parallel implementation of this algorithm uses p^2 processors in a $p \times p$ wraparound mesh topology to solve an $n \times n$ system matrix. The matrix is partitioned into submatrix blocks of size $\frac{n}{p} \times \frac{n}{p}$, assigned to each of the processors (see Fig.15).

Each iteration of the algorithm requires multiplications (in the term $\alpha_{ij}x_j$) and summation. Multiplications dominate execution time here, hence the parallelisation concentrates on them. The horizontal part of the mesh acts as a collection of circular pipelines

```
Master:
   right.outwhile(cond) {
     left = chanLast.request();
     right.send(data);
     processData();
     newData = left.receive();
   }
Worker:
   right.outwhile(left.inwhile) {
     right.send(data);
     processData();
     newData = left.receive();
   7
WorkerLast :
   left.inwhile {
     right = chanFirst.accept();
     right.send(data);
     processData();
     newData = left.receive();
   }
```

```
Master:
 <left,right>.outwhile(cond) {
   right.send(data);
   processData();
   newData = left.receive();
 }
Worker:
 right.outwhile(left.inwhile) {
   right.send(data);
   processData();
   newData = left.receive();
 }
WorkerLast :
 <left,right>.inwhile {
   right.send(data);
   processData();
   newData = left.receive();
 }
```

Fig. 12. Implementation of the ring topology, single-channel primitives only.



Data transfer

Fig. 14. Communication patterns in *n*-Body implementations.

Repeated session open/close

for multiplications. Their results are collected by the diagonal nodes, which perform the summation and the division by α_{ii} .

This gives the updated solution values for the iteration. These need to be communicated to other nodes for the next iteration. The vertical mesh connections are used for this purpose: the solution values are sent down by the diagonal node, and each worker node picks up the locally required solution values, and passes on the rest. The transmission wraps around at the bottom of the mesh, and stops at the node immediately above the diagonal, hence the lack of connectivity between the two in Fig.15.

```
Master (is also on the diagonal):
<under,right>.outwhile(
    hasNotConverged()) {
 prod = computeProducts();
  // horizontal ring, pass results
       to diagonal node
 ringData = prod;
  <left,right>.outwhile(count <
      nodesOnRow) {
   right.send(ringData);
   ringData = left.receive();
                                        }
   computeSums(ringData);
   count++;
 }
                                      }
 newX = computeDivision();
 under.send(newX);
}
```

```
Worker:
<under,right>.outwhile(<left,over
    >.inwhile) {
 prod = computeProducts();
 ringData = prod;
 right.outwhile(left.inwhile) {
   right.send(ringData);
                                        }
   ringData = left.receive();
 }
                                      }
 newX = over.receive();
 under.send(newX);
```

}

```
WorkerDiagonal :
<under,right>.outwhile(left.
    inwhile) {
 prod = computeProducts();
 ringData = prod;
 right.outwhile(left.inwhile) {
   right.send(ringData);
   ringData = left.receive();
   computeSums(ringData);
 newX = computeDivision();
 under.send(newX);
```

```
WorkerEastLast :
<right,left,over>.inwhile {
 prod = computeProducts();
 ringData = prod;
 <left,right>.inwhile {
   right.send(ringData);
   ringData = left.receive();
 newX = over.receive();
```



Fig. 15. Linear Equations Solver implementation using a wraparound mesh.

Note that contrary to the non-wraparound 2D-mesh of §3, the sink of this wellformed topology ($\S4.2$) is not the last node on the diagonal, but instead the node just above, called WorkerEastLast. This is because the diagonal nodes transmit updated values as explained above, and this transmission stops just before a complete wraparound. Fig.18 shows node ranks for the wraparound mesh topology, along with the other topologies presented in the paper.

A.3 *n*-Body Processes

This appendix gives the full definition of *n*-Body processes.

$$\begin{split} P_1 &\equiv \langle k_{(1,2)}, k_{(1,3)} \rangle.\texttt{outwhile}(e) \{ \overline{k_{(1,2)}} \langle \texttt{Particle}[] \rangle; k_{(1,3)}(x). \ \mathbf{0} \ \} \\ P_2 &\equiv k_{(2,3)}.\texttt{outwhile}(k_{(1,2)}.\texttt{inwhile}) \{ \overline{k_{(2,3)}} \langle \texttt{Particle}[] \rangle; k_{(1,2)}(x). \ \mathbf{0} \ \} \end{split}$$

$$P_3 \equiv \langle k_{(1,3)}, k_{(2,3)} \rangle . \texttt{inwhile} \{ k_{(1,3)} \langle \texttt{Particle} || \rangle; k_{(2,3)}(x) . \mathbf{0} \}$$

where the typing of the processes are (omitting end):

$$\begin{split} &\Gamma \vdash P_1 \triangleright \{k_{(1,2)} : ![![U]]^*, \ k_{(1,3)} : ![?[U]]^* \} \\ &\Gamma \vdash P_2 \triangleright \{k_{(1,2)}' : ?[?[U]]^*, \ k_{(2,3)} : ![![U]]^* \} \\ &\Gamma \vdash P_3 \triangleright \{k_{(1,3)}' : ?[![U]]^*, \ k_{(2,3)}' : ?[?[U]]^* \} \end{split}$$

A.4 Jacobi Processes

This appendix gives the full definition of Jacobi processes.

$$\begin{split} &P_{1} = P_{NW} = \langle k_{(1,2)}, k_{(1,4)} \rangle . \texttt{outwhile}(e) \{ \overline{k_{(1,2)}} \langle \texttt{double}[] \rangle; k_{(1,2)}(x) . \overline{k_{(1,4)}} \langle \texttt{double}[] \rangle; k_{(1,4)}(y) . \mathbf{0} \} \\ &P_{2} = P_{N} = \langle k_{(2,4)}, k_{(2,5)} \rangle . \texttt{outwhile}(k'_{(1,2)} . \texttt{inwhile}) \{ \\ & k'_{(1,2)}(x) . \overline{k'_{(1,2)}} \langle \texttt{double}[] \rangle; \overline{k_{(2,4)}} \langle \texttt{double}[] \rangle; k_{(2,4)}(y) . \overline{k_{(2,5)}} \langle \texttt{double}[] \rangle; k_{(2,5)}(z) . \mathbf{0} \} \\ &P_{4} = P_{NE} = k_{(4,7)} . \texttt{outwhile}(k'_{(2,4)} . \texttt{inwhile}) \{ \\ & k'_{(2,4)}(x) . \overline{k'_{(2,4)}} \langle \texttt{double}[] \rangle; \overline{k_{(4,7)}} \langle \texttt{double}[] \rangle; k_{(4,7)}(y) . \mathbf{0} \} \\ &P_{3} = P_{W} = \langle k_{(3,5)}, k_{(3,6)} \rangle . \texttt{outwhile}(k'_{(1,3)} . \texttt{inwhile}) \{ \\ & k'_{(1,3)}(x) . \overline{k'_{(2,4)}} \langle \texttt{double}[] \rangle; \overline{k_{(3,5)}} \langle \texttt{double}[] \rangle; k_{(3,5)}(y) . \overline{k_{(3,6)}} \langle \texttt{double}[] \rangle; k_{(3,6)}(z) . \mathbf{0} \} \\ &P_{5} = P_{C} = \langle k_{(5,7)}, k_{(5,8)} \rangle . \texttt{outwhile}(k'_{(2,5)}, k'_{(3,5)}) \land \texttt{inwhile}) \{ \\ & k'_{(2,5)}(w) . \overline{k'_{(2,5)}} \langle \texttt{double}[] \rangle; k'_{(3,5)}(x) . \overline{k'_{(3,5)}} \langle \texttt{double}[] \rangle; k_{(3,6)}(z) . \mathbf{0} \} \\ &P_{7} = P_{E} = k_{(7,9)} . \texttt{outwhile}(\langle k'_{(4,7)}, k'_{(5,7)} \rangle . \texttt{inwhile}) \{ \\ & k'_{(4,7)}(x) . \overline{k'_{(4,7)}} \langle \texttt{double}[] \rangle; k'_{(5,7)}(y) . \overline{k'_{(5,7)}} \langle \texttt{double}[] \rangle; k_{(5,8)}(z) . \mathbf{0} \} \\ &P_{6} = P_{SW} = k_{(6,8)} . \texttt{outwhile}(\langle k'_{(3,6)} . \texttt{inwhile}) \{ \\ & k'_{(4,7)}(x) . \overline{k'_{(4,7)}} \langle \texttt{double}[] \rangle; k'_{(5,7)}(y) . \overline{k'_{(5,7)}} \langle \texttt{double}[] \rangle; \overline{k_{(6,8)}} \langle \texttt{double}[] \rangle; k_{(6,8)}(y) . \mathbf{0} \} \\ &P_{8} = P_{5} = k_{(6,8)} . \texttt{outwhile}(\langle k'_{(3,6)} . \texttt{inwhile}) \{ \\ & k'_{(5,8)}(x) . \overline{k'_{(5,8)}} \langle \texttt{double}[] \rangle; k'_{(6,8)}(y) . \overline{k'_{(6,8)}} \langle \texttt{double}[] \rangle; k_{(6,8)}(z) . \mathbf{0} \} \\ &P_{9} = P_{SE} = \langle k'_{(7,9)}, k'_{(5,8)} \rangle . \texttt{inwhile}\{ k'_{(7,9)}(x) . \overline{k'_{(7,9)}} \langle \texttt{double}[] \rangle; k'_{(6,8)}(y) . \overline{k'_{(6,8)}} \langle \texttt{double}[] \rangle; k'_{(6,8)}(z) . \mathbf{0} \} \\ &P_{9} = P_{SE} = \langle k'_{(7,9)}, k'_{(8,9)} \rangle . \texttt{inwhile}\{ k'_{(7,9)}(x) . \overline{k'_{(7,9)}} \langle \texttt{double}[] \rangle; k'_{(6,8)}(y) . \overline{k'_{(6,8)}} \langle \texttt{double}[] \rangle \} \\ &P_{9} = P_{SE} = \langle k'_{(7,9)}, k''_{(8,9)} \rangle . \texttt{inwhile}\{ k'_{(7,9$$

where the typing of the processes are (omitting end):

$$\begin{split} & \Gamma \vdash P_{1} \triangleright \{k_{(1,2)} : ![!];?[]]^{*}, k_{(1,3)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{2} \triangleright \{k_{(1,2)}' : ?[?]] \cdot ![]]^{*}, k_{(2,4)} : ![!];?[]]^{*}, k_{(2,5)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{4} \triangleright \{k_{(2,4)}' : ?[?]] \cdot ![]]^{*}, k_{(4,7)} : ![!];?[]]^{*}, k_{(3,6)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{3} \triangleright \{k_{(1,3)}' : ?[?]] \cdot ![]]^{*}, k_{(3,5)} : ![!];?[]]^{*}, k_{(3,6)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{5} \triangleright \{k_{(2,5)}' : ?[?]] \cdot ![]]^{*}, k_{(3,5)} : ?[?]] \cdot ![]]^{*}, k_{(5,7)} : ![!];?[]]^{*}, k_{(5,8)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{7} \triangleright \{k_{(4,7)}' : ?[?]] \cdot ![]]^{*}, k_{(5,7)}' : ?[?]] \cdot ![]]^{*}, k_{(7,9)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{6} \triangleright \{k_{(3,6)} : ?[?]] \cdot ![]]^{*}, k_{(6,8)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{8} \triangleright \{k_{(5,8)}' : ?[?]] \cdot ![]]^{*}, k_{(6,8)}' : ?[?]] \cdot ![]]^{*}, k_{(8,9)} : ![!];?[]]^{*} \} \\ & \Gamma \vdash P_{9} \triangleright \{k_{(7,9)}' : ?[?]] \cdot ![]]^{*}, k_{(8,9)}' : ?[?]] \cdot ![]^{*} \} \end{split}$$

We can easily check Jacobi processes satisfy the well-formed topology condition.

B Appendix to Section 4

B.1 Structural Congruence and Typing Rules

This section lists the omitted definitions from Section 4. Structural congruence rules are defined in Fig. 16; full typing rules can be found in Fig. 17. In this context, fn(Q) denotes a set of free shared and session channels, and fpv(D) stands for a set of free process variables. In the typing system, Δ is complete means that Δ includes only end or \bot . Further explanations can be found in [16].

$$P \equiv Q \text{ if } P \equiv_{\alpha} Q \quad P \mid \mathbf{0} \equiv P \quad P \mid Q \equiv Q \mid P \quad (P \mid Q) \mid R \equiv P \mid (Q \mid R)$$

$$(vu)P \mid Q \equiv (vu)(P \mid Q) \quad \text{ if } u \notin \text{fn}(Q)$$

$$(vu) \mathbf{0} \equiv \mathbf{0} \quad \text{def } D \text{ in } \mathbf{0} \equiv \mathbf{0} \quad \mathbf{0} \text{ ; } P \equiv P$$

$$(vu)\text{def } D \text{ in } P \equiv \text{def } D \text{ in } (vu)P \quad \text{ if } u \notin \text{fn}(D)$$

$$(\text{def } D \text{ in } P) \mid Q \equiv \text{def } D \text{ in } (P \mid Q) \quad \text{ if } \text{fpv}(D) \cap \text{fpv}(Q) = \emptyset$$

$$\text{def } D \text{ in } (\text{def } D' \text{ in } P) \equiv \text{def } D \text{ and } D' \text{ in } P \quad \text{ if } \text{fpv}(D) \cap \text{fpv}(D') = \emptyset.$$

Fig. 16. Structural congruence.

Definition B.1. A process is under a well-formed intermediate topology if:

1. (inwhile and outwhile)

$$\begin{split} P_{1} &= \langle k_{(1,2)}, \dots, k_{(1,N)} \rangle . \textit{outwhile}(e) \{ Q_{1}[k_{(1,2)}, \dots, k_{(1,N)}] \} \\ P_{i} &= \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle . \textit{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle . \textit{inwhile}) \{ Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}] \} | k_{(i,i+1)} \dagger [b] | k_{(1,i)} \dagger [b] | \dots | k_{(i-1,i)} \dagger [b] \\ when i \in \{2..M - 1\}, b \in \{\textit{true}, \textit{false}\} \\ P_{j} &= \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle . \textit{inwhile}\{Q_{j}[k_{(1,j)}, \dots, k_{(j-1,j)}]\}) | k_{(1,j)} \dagger [b] | \dots | k_{(j-1,j)} \dagger [b] \\ when j \in \{M..N\} \forall b \in \{\textit{true}\} \text{ or } \forall b \in \{\textit{false}\} \\ and \\ \Gamma \vdash Q_{1} \triangleright \{k_{(1,2)} : T_{(1,2)}, \dots, k_{(1,N)} : T_{(1,N)}\} \\ \Gamma \vdash Q_{i} \triangleright \{k_{(i,i+1)} : T_{(i,i+1)}, \dots, k_{(i,N)} : T_{(i,N)}, k_{(1,i)} : T_{(1,i)}^{\prime} \dagger, \dots, k_{(i-1,i)} : T_{i-1}^{\prime} i^{\dagger} \} \\ \Gamma \vdash Q_{j} \triangleright \{k_{(1,j)} : T_{(1,j)}^{\prime} \dagger, k_{(j-1,j)} : T_{(j-1,j)}^{\prime} \dagger \} \\ and \\ \Gamma \vdash Q_{1} | Q_{2} | \dots | Q_{n} \triangleright \{\tilde{k}: \tilde{\bot}^{\dagger}\} \\ with T_{(i,j)} = \overline{T_{(i,j)}^{\prime}} \end{split}$$

- 2. (sequencing) $P_i = Q_{1i}; ...; Q_{mi}$ where $(Q_{j1} | Q_{j2} | \cdots | Q_{jn})$ conforms a well-formed intermediate topology for each $1 \le j \le m$.
- 3. (base) (1) session actions in P_i follows the order of the index (e.g. the session actions at $k_{(i,j)}$ happens before $k_{(h,g)}$ if (i,j) < (h,g)), then the rest is a base process P'_i ; and (2) P_i includes neither shared session channels, inwhile nor outwhile.

Fig. 17. Typing rules.

B.2 Well-Formed Ring and Mesh Topologies



Fig. 18. Ring, mesh, and wraparound mesh topologies, with rank annotations.

We define well-formed ring and mesh topologies. We can check that they conform to the general definition of well-formed topology (Definition 4.1). Fig. 18 shows the rank of each process for each topology, indicating how both rings and meshes map to the general definition.

Definition B.2. A process group

$$P_{NW} | P_{NE} | P_{SW} | P_{SE} | P_{N_1} \dots | P_{N_m} | P_{S_1} \dots | P_{S_m} | P_{E_1} \dots | P_{E_n} | P_{W_1} \dots | P_{W_n} | P_{C_22} \dots | P_{C_{n-1m-1}}$$

conforms to a well-formed mesh topology if:

$$\begin{split} P_{NW} = & \langle t_1, l_1 \rangle. \textit{outwhile}(e) \{ Q_{NW}[t_1, l_1] \} \\ P_{N_j} = & \langle t_{j+1}, vc_{1j} \rangle. \textit{outwhile}(t_j. \textit{inwhile}) \{ Q_{N_j}[t_{j+1}, vc_{1j}, t_j] \} \\ P_{NE} = & r_1. \textit{outwhile}(t_m. \textit{inwhile}) \{ Q_{NE}[r_1, t_m] \} \\ P_{W_i} = & \langle hc_{i1}, l_{i+1} \rangle. \textit{outwhile}(l_i. \textit{inwhile}) \{ Q_W[hc_{i1}, l_{i+1}, l_i] \} \\ P_{C_{ij}} = & \langle vc_{i+1}, hc_{i}, j_{i+1} \rangle. \textit{outwhile}(\langle hc_{ij}, vc_{ij} \rangle. \textit{inwhile}) \{ Q_{C_{ij}}[vc_{i+1}, hc_{i}, j_{i+1}, hc_{ij}, vc_{ij}] \} \\ P_{E_i} = & r_{i+1}. \textit{outwhile}(\langle hc_{im}, r_i \rangle. \textit{inwhile}) \{ Q_{E_i}[r_{i+1}, hc_{im}, r_i] \} \\ P_{SW} = & b_1. \textit{outwhile}(l_n. \textit{inwhile}) \{ Q_{SW}[b_1, l_n] \} \\ P_{S_j} = & b_{j+1}. \textit{outwhile}(\langle b_j, vc_{nj} \rangle. \textit{inwhile}) \{ Q_{S_j}[b_{j+1}, b_j, vc_{nj}] \} \\ P_{SE} = & \langle b_m, r_n \rangle. \textit{inwhile} \{ Q_{SE}[b_m, r_n] \} \end{split}$$

where $1 \le i \le n, 1 \le j \le m$, and

$$\begin{split} & \Gamma \vdash Q_{NW} \triangleright \{t_{1} \colon T_{t_{1}}, t_{1} \colon T_{t_{1}}\} \\ & \Gamma \vdash Q_{N_{j}} \triangleright \{t_{j+1} \colon T_{t_{j+1}}, vc_{1j} \colon T_{vc_{1j}}, t_{j} \colon T_{t_{j}}'\} \\ & \Gamma \vdash Q_{NE} \triangleright \{r_{1} \colon T_{r_{1}}, t_{m} \colon T_{t_{m}}'\} \\ & \Gamma \vdash Q_{W} \triangleright \{hc_{i1} \colon T_{hc_{i1}}, l_{i+1} \colon T_{l_{i+1}}, l_{i} \colon T_{l_{i}}'\} \\ & \Gamma \vdash Q_{C_{ij}} \triangleright \{vc_{i+1} \ j \colon T_{vc_{i+1} \ j}, hc_{i} \ j+1 \colon T_{hc_{i} \ j+1}, hc_{ij} \colon T_{hc_{ij}}', vc_{ij} \colon T_{vc_{ij}}'\} \\ & \Gamma \vdash Q_{E_{i}} \triangleright \{r_{i+1} \colon T_{r_{i+1}}, hc_{im} \colon T_{hc_{im}}, r_{i} \colon T_{r_{i}}'\} \\ & \Gamma \vdash Q_{SW} \triangleright \{b_{1} \colon T_{b_{1}}, l_{n} \colon T_{l_{n}}'\} \\ & \Gamma \vdash Q_{SE} \triangleright \{b_{m} \colon T_{b_{m}}', r_{n} \colon T_{r_{n}}'\} \\ & \text{with} \quad \overline{T_{i}} = T_{i}' \end{split}$$

C Appendix - Proofs

Definition C.1. Sequential composition of session type are defined as [5]:

 $\tau; \alpha = \begin{cases} \tau.\alpha & \text{if } \tau \text{ is a partial session type and } \alpha \text{ is a completed session type} \\ \bot & \text{otherwise} \end{cases}$ $\Delta; \Delta' = \Delta \setminus \operatorname{dom}(\Delta') \cup \Delta' \setminus \operatorname{dom}(\Delta) \cup \{k: \Delta(k) \setminus \operatorname{end}; \Delta'(k) \mid k \in \operatorname{dom}(\Delta) \cap \operatorname{dom}(\Delta')\}$

The first rule concatenates a partial session type τ with a completed session type α to form a new (completed) session type. The second rule can be decomposed to three parts:

- 1. $\Delta \setminus \operatorname{dom}(\Delta')$ extracts session types with sessions unique in Δ
- 2. $\Delta' \setminus \text{dom}(\Delta)$ extracts session types with sessions unique in Δ'
- 3. $\{k: \Delta(k) \setminus \text{end}; \Delta'(k) \mid k \in \text{dom}(\Delta) \cap \text{dom}(\Delta')\}$ modifies session types with a common session k in Δ and Δ' by removing end type from $\Delta(k)$ and concatenates the modified $\Delta(k)$ (which is now a partial session type) with $\Delta'(k)$ as described in the first rule.

Example C.1. Suppose $\Delta = \{k_1 : \varepsilon \text{.end}, k_2 : ![nat].end\}$ and $\Delta' = \{k_2 : ?[bool].end, k_3 : ![bool].end\}$. Since k_1 is unique in Δ and k_3 is unique in Δ' , we have

 $\Delta \setminus \operatorname{dom}(\Delta') = \{k_1 : \varepsilon.\operatorname{end}\} \text{ and } \Delta' \setminus \operatorname{dom}(\Delta) = \{k_3 : ![\operatorname{bool}].\operatorname{end}\}$

A new session type is constructed by removing end in $\Delta(k_2)$, so the composed set of mappings is

$$\Delta; \Delta' = \{k_1 \colon \varepsilon.\mathsf{end}, k_2 \colon ![\mathsf{nat}]; ?[\mathsf{bool}].\mathsf{end}, k_3 \colon ![\mathsf{bool}].\mathsf{end}\}$$

Definition C.2. Parallel composition of session and runtime type is defined as:

$$\begin{split} \Delta \circ \Delta' = &\Delta \setminus \operatorname{dom}(\Delta') \cup \Delta' \setminus \operatorname{dom}(\Delta) \cup \{k \colon \beta \circ \beta' \mid \Delta(k) = \beta \text{ and } \Delta'(k) = \beta'\} \\ \text{where } \beta \circ \beta' \colon \begin{cases} \alpha \circ \dagger = \alpha^{\dagger} \\ \alpha \circ \overline{\alpha} = \bot \\ \alpha \circ \overline{\alpha}^{\dagger} = \bot^{\dagger} \end{cases} \end{split}$$

The parallel composition relation \circ is commutative as the order of composition do not impact the end result.

We now present some auxiliary results for subject reduction, the following proofs are modified from [22], and adapted to our updated typing system.

Lemma C.1 (Weakening Lemma). Let $\Gamma \vdash P \triangleright \Delta$.

- 1. If $X \notin \text{dom}(\Gamma)$, then $\Gamma \cdot X : S\alpha \vdash P \triangleright \Delta$.
- 2. If $a \notin \text{dom}(\Gamma)$, then $\Gamma \cdot a : S \vdash P \triangleright \Delta$.
- 3. If $k \notin \text{dom}(\Delta)$ and $\alpha = \perp$ or $\alpha = \varepsilon$.end, then $\Gamma \vdash P \triangleright \Delta \cdot k : \alpha$.

Proof. A simple induction on the derivation tree of each sequent. For 3, we note that in [INACT] and [VAR], Δ contains only ε .end.

Lemma C.2 (Strengthening Lemma). Let $\Gamma \vdash P \triangleright \Delta$.

- *1. If* $X \notin \text{fpv}(P)$ *, then* $\Gamma \setminus X \vdash P \triangleright \Delta$ *.* 2. If $a \notin \operatorname{fn}(P)$, then $\Gamma \setminus a \vdash P \triangleright \Delta$.
- *3. If* $k \notin \text{fn}(P)$ *, then* $\Gamma \vdash P \triangleright \Delta \setminus k$ *.*

Proof. Standard.

Lemma C.3 (Channel Lemma).

1. If $\Gamma \vdash P \triangleright \Delta \cdot k$: α *and* $k \notin fn(P)$ *, then* $\alpha = \bot, \varepsilon$.end. *2. If* $\Gamma \vdash P \triangleright \Delta$ *and* $k \in fn(P)$ *, then* $k \in dom(\Delta)$.

Proof. A simple induction on the derivation tree for each sequent.

We omit the standard renaming properties of variables and channels, but present the Substitution Lemma for names. Note that we do *not* require a substitution lemma for channels or process variables, for they are not communicated.

Lemma C.4 (Substitution Lemma). *If* $\Gamma \cdot x$: $S \vdash P \triangleright \Delta$ *and* $\Gamma \vdash c$: S, *then* $\Gamma \vdash P\{c/x\} \triangleright \Delta$

Proof. Standard.

We write $\Delta \prec \Delta'$ if we obtain Δ' from Δ by replacing $k_1: \varepsilon.\text{end}, ..., k_n: \varepsilon.\text{end} (n \ge 0)$ in Δ by $k_1: \bot, ..., k_n: \bot$. If $\Delta \prec \Delta'$, we can obtain Δ' from Δ by applying the [Bot]-rule zero or more times.

Theorem C.1. Subject congruence is defined by

$$\Gamma \vdash P \triangleright \Delta$$
 and $P \equiv P'$ implies $\Gamma \vdash P' \triangleright \Delta$

Proof. Case $P \mid \mathbf{0} \equiv P$. We show that if $\Gamma \vdash P \mid \mathbf{0} \triangleright \Delta$, then $\Gamma \vdash P \triangleright \Delta$. Suppose

 $\Gamma \vdash P \triangleright \Delta_1$ and $\Gamma \vdash \mathbf{0} \triangleright \Delta_2$.

with $\Delta_1 \circ \Delta_2 = \Delta$. Note that Δ_2 only contains ε end or \bot , hence we can set: $\Delta_1 = \Delta'_1 \circ \{k : \varepsilon \text{.end}\}$ and $\Delta_2 = \Delta'_2 \cdot \{k : \varepsilon \text{.end}\}$ with $\Delta'_1 \circ \Delta'_2 = \Delta'_1 \cdot \Delta'_2$ and $\Delta = \Delta'_1 \cdot \Delta'_2 \cdot \{k : \bot\}$. Then by the [BoT]-rule, we have:

$$\Gamma \vdash P \triangleright \Delta'_1 \cdot \{k:\perp\}$$

Notice that, given the form of Δ above, we know that $dom(\Delta'_2) \cap dom(\Delta'_1) \cdot \{k: \perp\}) = \emptyset$. Hence by applying Weakening, we have:

$$\Gamma \vdash P \triangleright \Delta'_1 \cdot \Delta'_2 \cdot \{k:\bot\}$$

as required.

For the other direction, we set $\Delta = \emptyset$ in [INACT].

Case $P \mid Q \equiv Q \mid P.\circ$ relation is commutative by the definition of \circ (Definition C.2) **Case** $(P \mid Q) \mid R \equiv P \mid (Q \mid R)$. To show $(P \mid Q) \mid R \equiv P \mid (Q \mid R)$, where

$$\Gamma \vdash P \triangleright \Delta_1 \quad \Gamma \vdash Q \triangleright \Delta_2 \quad \Gamma \vdash R \triangleright \Delta_3$$

We assume $(\Delta_1 \circ \Delta_2) \circ \Delta_3$ is defined Suppose $k: \beta_1 \in \Delta_1$ and $k: \beta_2 \in \Delta_2$, then we have

$$\begin{cases} \beta_1 = \alpha \quad \beta_2 = \dagger \\ \beta_1 = \alpha \quad \beta_2 = \overline{\alpha} \\ \beta_1 = \alpha \quad \beta_2 = \overline{\alpha}^\dagger \\ \beta_1 = \dagger \quad \beta_2 = \bot \end{cases}$$

Now suppose $k: \beta_3 \in \Delta_3$, if $\beta_1 = \alpha$ $\beta_2 = \dagger$, then $\beta_3 = \overline{\alpha}$

$$(\beta_1 \circ \beta_2) \circ \beta_3 = (\{k: \alpha\} \circ \{k: \dagger\}) \circ \{k: \overline{\alpha}\} = \{k: \perp^{\dagger}\}$$
$$\equiv \beta_1 \circ (\beta_2 \circ \beta_3) = \{k: \alpha\} \circ (\{k: \dagger\} \circ \{k: \overline{\alpha}\}) = \{k: \perp^{\dagger}\}$$

if $\beta_1 = \alpha$ $\beta_2 = \overline{\alpha}$, then $\beta_3 = \dagger$

$$(\beta_1 \circ \beta_2) \circ \beta_3 = (\{k: \alpha\} \circ \{k: \overline{\alpha}\}) \circ \{k: \dagger\} = \{k: \perp^{\dagger}\} \equiv \beta_1 \circ (\beta_2 \circ \beta_3) = \{k: \alpha\} \circ (\{k: \overline{\alpha}\} \circ \{k: \dagger\}) = \{k: \perp^{\dagger}\}$$

in all other cases, $k \notin \text{dom}(\Delta_3)$ and therefore no parallel composition is possible.

Case $(vu)P | Q \equiv (vu)(P | Q)$ if $u \notin fn(Q)$. The case when *u* is a name is standard. Suppose *u* is channel *k* and assume $\Gamma \vdash (vk)(P | Q) \triangleright \Delta$. We have

$$\frac{\Gamma \vdash P \triangleright \Delta_1' \quad \Gamma \vdash Q \triangleright \Delta_2'}{\Gamma \vdash P \mid Q \triangleright \Delta' \cdot k \colon \bot}$$

with $\Delta' \cdot k : \bot = \Delta'_1 \circ \Delta'_2$ and $\Delta' \prec \Delta$ by [BOT]. First notice that k can be in either Δ'_i or in both. The interesting case is when it occurs in both; from Lemma C.3(1) and the fact that $k \notin \operatorname{fn}(Q)$ we know that $\Delta'_1 = \Delta_1 \cdot k : \varepsilon$.end and $\Delta'_2 = \Delta_2 \cdot k : \varepsilon$.end. Then, by applying the [BOT]-rule to k in P, we have $\Gamma \vdash P \triangleright \Delta_1 \cdot k : \bot$, and by applying [CRES] we obtain $\Gamma \vdash (vk)P \triangleright \Delta_1$. On the other hand, by Strengthening, we have $\Gamma \vdash Q \triangleright \Delta_2$. Then, the application of [CONC] yields $\Gamma \vdash (vk)P \mid Q \triangleright \Delta'$. Then by applying the [BOT]-rule, we obtain $\Gamma \vdash (vk)P \mid Q \triangleright \Delta$, as required. The other direction is easy.

Case $(vu) \mathbf{0} \equiv \mathbf{0}$. Standard by Weakening and Strengthening.

Case def D in $0 \equiv 0$. Similar to the first case using Weakening and Strengthening.

Case (vu) def D in $P \equiv \text{def } D$ in (vu)P if $u \notin \text{fn}(D)$. Similar to the scope opening case using Weakening and Strengthening.

Case $(\text{def } D \text{ in } P) | Q \equiv \text{def } D \text{ in } (P | Q) \text{ if } \text{fpv}(D) \cap \text{fpv}(Q) = \emptyset$. Similar with the scope opening case using Weakening and Strengthening.

Case 0 ; $P \equiv P$. We show that if $\Gamma \vdash 0$; $P \triangleright \Delta$, then $\Gamma \vdash P \triangleright \Delta$. Suppose

$$\Gamma \vdash \mathbf{0} \triangleright \Delta_1$$
 and $\Gamma \vdash P \triangleright \Delta_2$.

with $\Delta_1; \Delta_2 = \Delta$. Δ_2 only contains ε .end or \bot , by definition of sequential composition (Definition C.1), $\Delta(k) = \Delta_1(k).\Delta_2(k) = \varepsilon.\Delta_2(k) = \Delta_2(k)$ as required.

Theorem 4.1 (Subject reduction) The following subject reduction rules hold for a *well-formed topology* (Definition 4.1).

$$\Gamma \vdash P \triangleright \Delta \text{ and } P \longrightarrow P' \text{ implies } \Gamma \vdash P' \triangleright \Delta' \quad \text{such that} \begin{array}{c} \Delta(k) = \alpha \Rightarrow \begin{cases} \Delta'(k) = \alpha \\ \Delta'(k) = \alpha^{\dagger} \end{cases} \\ \Delta(k) = \alpha^{\dagger} \Rightarrow \begin{cases} \Delta'(k) = \alpha \\ \Delta'(k) = \alpha \\ \Delta'(k) = \alpha^{\dagger} \end{cases} \end{array}$$

Under a well-formed intermediate topology (Definition B.1)

 $\Gamma \vdash P \triangleright \Delta \text{ and } P \longrightarrow^* P' \text{ implies } \Gamma \vdash P' \triangleright \Delta' \quad \text{ such that} \qquad \begin{aligned} \Delta(k) &= \alpha \Rightarrow \begin{cases} \Delta'(k) = \alpha \\ \Delta'(k) = \alpha^{\dagger} \\ \Delta(k) = \alpha \\ \Delta'(k) = \alpha \\ \Delta'(k) = \alpha^{\dagger} \end{aligned}$

Proof. We assume that

$$\Gamma \vdash e \triangleright S$$
 and $e \downarrow c$ implies $\Gamma \vdash c \triangleright S$ (1)

and prove the result by induction on the last rule applied. For simplicity, assume all nodes are fully connected.

Case inwhile/outwhile for N processes $(v\tilde{k})(P_1 \mid \ldots \mid P_N)$. Assume well-formed topology (Definition 4.1) Case $E[e] \longrightarrow E[true]$

By [Ow1],

$$\begin{split} (\mathbf{v}\tilde{k}) & (\langle k_{(1,2)}, \dots, k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_1[k_{(1,2)}, \dots, k_{(1,N)}] \} \\ & | \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle. \texttt{inwhile}) \{ \\ & \mathcal{Q}_i[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}] \} \text{ when } i \in \{2..M-1\} \\ & | \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle. \texttt{inwhile}\{ \mathcal{Q}_j[k_{(1,j)}, \dots, k_{(j-1,j)}] \}) \text{ when } j \in \{M..N\} \\ \rightarrow^* (\mathbf{v}\tilde{k}) (\mathcal{Q}_1[k_{(1,2)}, \dots, k_{(1,N)}]; \langle k_{(1,2)}, \dots, k_{(1,N)} \rangle. \texttt{outwhile}(e') \{ \mathcal{Q}_1[k_{(1,2)}, \dots, k_{(1,N)}] \} \\ & | k_{(1,2)} \dagger [\texttt{true}] | \dots | k_{(1,N)} \dagger [\texttt{true}] \\ & | \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle. \texttt{inwhile}) \{ \\ & \mathcal{Q}_i[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}] \} \text{ when } i \in \{2..M-1\} \end{split}$$

$$|\langle k_{(1,j)},\ldots,k_{(j-1,j)}\rangle.\texttt{inwhile}\{Q_j[k_{(1,j)},\ldots,k_{(j-1,j)}]\}) \text{ when } j \in \{M..N\}$$

$$\begin{split} & \Gamma \vdash (Q_{1};P_{1} \mid k_{(1,2)} \dagger | \texttt{true} | | \dots | k_{(1,N)} \dagger | \texttt{true} | P_{i \in 2..M-1} \mid P_{j \in M..N}) \\ & \triangleright \{k_{(1,2)} \colon T_{(1,2)}; ! [T_{(1,2)}]^{*} \circ ? [T'_{(1,2)}]^{*\dagger}, \dots, k_{(1,N)} \colon T_{(1,N)}; ! [T_{(1,N)}]^{*} \circ ? [T'_{(1,N)}]^{*\dagger}, \\ & k_{(i,i+1)} \colon ! [T_{(i,i+1)}]^{*} \circ ? [T'_{(i,i+1)}]^{*}, \dots, k_{(i,N)} \colon ! [T_{(i,N)}]^{*} \circ ? [T'_{(i,N)}]^{*} \} \end{split}$$

By [IwE1],

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 \begin{array}{l} (v\tilde{k}) \ (\mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}]; \langle k_{(1,2)},\ldots,k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}] \} \\ & | k_{(1,2)} \dagger [\texttt{true}] | \ldots | k_{(1,N)} \dagger [\texttt{true}] \\ & | \langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)},\ldots,k_{(i-1,i)} \rangle. \texttt{inwhile}) \{ \\ & \mathcal{Q}_{i}[k_{(i,i+1)},\ldots,k_{(i,N)},k_{(1,i)},\ldots,k_{(i-1,i)}] \} \ \texttt{when} \ i \in \{2..M-1\} \\ & | \langle k_{(1,j)},\ldots,k_{(j-1,j)} \rangle. \texttt{inwhile}\{ \mathcal{Q}_{j}[k_{(1,j)},\ldots,k_{(j-1,j)}] \}) \ \texttt{when} \ j \in \{M..N\} \\ \longrightarrow^{*} (v\tilde{k}) \ (\mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}]; \langle k_{(1,2)},\ldots,k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}] \} \\ & | k_{(1,3)} \dagger [\texttt{true}] | \ldots | k_{(1,N)} \dagger [\texttt{true}] \\ & | \langle k_{(2,3)},\ldots,k_{(2,N)} \rangle. \texttt{outwhile}(\texttt{true}) \{ \mathcal{Q}_{2}[k_{(2,3)},\ldots,k_{(2,N)},k_{(1,2)}] \} \\ & | \langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)},\ldots,k_{(i-1,i)} \rangle. \texttt{inwhile}) \{ \\ & \mathcal{Q}_{i}[k_{(i,i+1)},\ldots,k_{(i,N)},k_{(1,i)},\ldots,k_{(i-1,i)}] \} \ \texttt{when} \ i \in \{3..M-1\} \\ & | \langle k_{(1,j)},\ldots,k_{(j-1,j)} \rangle. \texttt{inwhile}\{ \mathcal{Q}_{j}[k_{(1,j)},\ldots,k_{(j-1,j)}] \} ) \ \texttt{when} \ j \in \{M..N\} \end{array}
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\begin{split} & \Gamma \vdash (Q_1; P_1 \mid P_{i \in 2..M-1} \mid P_{j \in M..N} \mid k_{(1,3)} \dagger [\texttt{true}] \mid \ldots \mid k_{(1,N)} \dagger [\texttt{true}]) \\ & \triangleright \{k_{(1,2)} \colon T_{(1,2)}; ![T_{(1,2)}]^* \circ T_{(1,2)}'; ?[T_{(1,2)}']^*, \\ & k_{(1,3)} \colon T_{(1,3)}; ![T_{(1,3)}]^* \circ T_{(1,3)}'; ?[T_{(1,3)}']^{*\dagger}, \ldots, k_{(1,N)} \colon T_{(1,N)}; ![T_{(1,N)}]^* \circ T_{(1,N)}'; ?[T_{(1,N)}']^{*\dagger}, \\ & k_{(2,3)} \colon ![T_{(2,3)}]^* \circ ?[T_{(2,3)}']^*, \ldots, k_{(N-1,N)} \colon ![T_{(N-1,N)}]^* \circ ?[T_{(N-1,N)}']^* \end{split}
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By [Ow1],

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 \begin{array}{l} (v\tilde{k}) \ (\mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}]; \langle k_{(1,2)},\ldots,k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}] \} \\ \quad | k_{(1,3)} \dagger [\texttt{true}] \mid \ldots \mid k_{(1,N)} \dagger [\texttt{true}] \\ \quad | \langle k_{(2,3)},\ldots,k_{(2,N)} \rangle. \texttt{outwhile}(\texttt{true}) \{ \mathcal{Q}_{2}[k_{(2,3)},\ldots,k_{(2,N)},k_{(1,2)}] \} \\ \quad | \langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle. \texttt{outwhile}(\texttt{true}) \{ \mathcal{Q}_{2}[k_{(2,3)},\ldots,k_{(2,N)},k_{(1,2)}] \} \\ \quad | \langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)},\ldots,k_{(i-1,i)} \rangle] \texttt{ when } i \in \{3..M-1\} \\ \quad | \langle k_{(1,j)},\ldots,k_{(j-1,j)} \rangle. \texttt{inwhile} \{ \mathcal{Q}_{j}[k_{(1,j)},\ldots,k_{(j-1,j)}] \} \texttt{ when } i \in \{3..M-1\} \\ \quad | \langle k_{(1,j)},\ldots,k_{(1,N)}]; \langle k_{(1,2)},\ldots,k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}] \} \\ \longrightarrow^{*} (v\tilde{k}) \ (\mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}]; \langle k_{(1,2)},\ldots,k_{(1,N)} \rangle. \texttt{outwhile}(e) \{ \mathcal{Q}_{1}[k_{(1,2)},\ldots,k_{(1,N)}] \} \\ \mid k_{(2,3)} \dagger [\texttt{true}] \mid \ldots \mid k_{(2,N)} \dagger [\texttt{true}] \mid k_{(1,3)} \dagger [\texttt{true}] \mid \ldots \mid k_{(1,N)} \\ \mid \mathcal{Q}_{2}[k_{(2,3)},\ldots,k_{(2,N)},k_{(1,2)}]; \ \langle k_{(2,3)},\ldots,k_{(2,N)} \rangle. \texttt{outwhile}(k_{(1,2)}.\texttt{inwhile}) \{ \\ \mathcal{Q}_{2}[k_{(2,3)},\ldots,k_{(2,N)},k_{(1,2)}] \} \\ \mid \langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle. \texttt{outwhile}(\langle k_{(1,i)},\ldots,k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ \mathcal{Q}_{i}[k_{(i,i+1)},\ldots,k_{(i,N)},k_{(1,i)},\ldots,k_{(i-1,i)}] \} \texttt{ when } i \in \{3..M-1\} \\ \mid \langle k_{(1,j)},\ldots,k_{(j-1,j)} \rangle.\texttt{inwhile} \{ \mathcal{Q}_{j}[k_{(1,j)},\ldots,k_{(j-1,j)}] \} \texttt{ when } i \in \{M..N\} \end{aligned}
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$$\begin{split} &\Gamma \vdash (\mathcal{Q}_{1}; P_{1} \mid \mathcal{Q}_{2}; P_{2} \mid P_{i \in 2..M-1} \mid P_{j \in M..N} \mid k_{(2,3)} \dagger [\texttt{true}] \mid \dots \mid k_{(2,N)} \dagger [\texttt{true}] \mid k_{(1,3)} \dagger [\texttt{true}] \mid \dots \mid k_{(N-1,N)} \dagger [\texttt{true}]) \\ & \triangleright \{k_{(1,2)} \colon T_{(1,2)}; ![T_{(1,2)}]^{*} \circ T'_{(1,2)}; ?[T'_{(1,2)}]^{*}, \\ & k_{(2,3)} \colon T_{(2,3)}; ![T_{(2,3)}]^{*} \circ T'_{(2,3)}; ?[T'_{(2,3)}]^{*\dagger}, \dots, k_{(2,N)} \colon T_{(2,N)}; ![T_{(2,N)}]^{*} \circ T'_{(2,N)}; ?[T'_{(2,N)}]^{*\dagger} \\ & k_{(1,3)} \colon T_{(1,3)}; ![T_{(1,3)}]^{*} \circ T'_{(1,3)}; ?[T'_{(1,3)}]^{*\dagger}, \dots, k_{(1,N)} \colon T_{(1,N)}; ![T_{(1,N)}]^{*} \circ T'_{(1,N)}; ?[T'_{(1,N)}]^{*\dagger}, \\ & k_{(3,4)} \colon ![T_{(3,4)}]^{*} \circ ?[T'_{(3,4)}]^{*}, \dots, k_{(N-1,N)} \colon ![T_{(N-1,N)}]^{*} \circ ?[T'_{(N-1,N)}]^{*} \end{split}$$

By repeatedly apply [Ow1] and [IwE1] as above on processes $P_3..P_{M-1}$

$$\begin{split} & \Gamma \vdash (Q_{1};P_{1} \mid Q_{i};P_{i \in 2..M-1} \mid P_{j \in M..N} \mid k_{(1,j)} \dagger [\texttt{true}] \mid \ldots \mid k_{(j-1,j)} \dagger [\texttt{true}]) \\ & \triangleright \{k_{(1,2)} \colon T_{(1,2)}; ![T_{(1,2)}]^{*} \circ T_{(1,2)}'; ?[T_{(1,2)}']^{*}, \ldots, k_{(1,N)} \colon T_{(1,N)}; ![T_{(1,N)}]^{*} \circ T_{(1,N)}'; ?[T_{(1,N)}']^{*}, \\ & k_{(i,i+1)} \colon T_{(i,i+1)}; ![T_{(i,i+1)}]^{*} \circ T_{(i,i+1)}'; ?[T_{(i,i+1)}']^{*}, \ldots, k_{(i,M-1)} \colon T_{(i,M-1)}; ![T_{(i,M-1)}]^{*} \circ T_{(i,M-1)}'; ?[T_{i,M-1}']^{*}, \\ & k_{(1,j)} \colon ![T_{(1,j)}]^{*} \circ ?[T_{(1,j)}']^{*\dagger}, \ldots, k_{(j-1,j)} \colon ![T_{(j-1,j)}]^{*} \circ ?[T_{(j-1,j)}']^{*\dagger} \} \end{split}$$

Finally apply [Iw1],

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 \begin{array}{l} (\mathbf{v}\tilde{k}) \left( Q_{1}[k_{(1,2)}, \dots, k_{(1,N)}]; \langle k_{(1,2)}, \dots, k_{(1,N)} \rangle. \mathbf{outwhile}(e) \{ Q_{1}[k_{(1,2)}, \dots, k_{(1,N)}] \} \\ & | k_{(1,j)} \dagger [\mathbf{true}] \mid \dots \mid k_{(j-1,j)} \dagger [\mathbf{true}] \\ & | Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}]; \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle. \mathbf{outwhile}(k_{(1,i)}, \dots, k_{(i-1,i)}.\mathbf{inwhile}) \{ Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}] \} \text{ when } i \in \{2..M-1\} \\ & | \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle. \mathbf{inwhile}\{ Q_{j}[k_{(1,j)}, \dots, k_{(j-1,j)}] \} \text{ when } i \in \{2..M-1\} \\ & | \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle. \mathbf{inwhile}\{ Q_{j}[k_{(1,j)}, \dots, k_{(j-1,j)}] \} \text{ when } j \in \{M..N\} \\ \longrightarrow^{*} (\mathbf{v}\tilde{k}) \left( Q_{1}[k_{(1,i)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}]; \\ & \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle. \mathbf{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle. \mathbf{inwhile}) \} \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}] \} \text{ when } i \in \{2..M-1\} \\ & | Q_{j}[k_{(1,j)}, \dots, k_{(j-1,j)}]; \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle. \mathbf{inwhile}\{ Q_{j}[k_{(1,j)}, \dots, k_{(j-1,j)}] \} \text{ when } j \in \{M..N\} \end{array}
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\begin{split} & \Gamma \vdash (Q_{1}; P_{1} \mid Q_{i}; P_{i \in 2...M-1} \mid Q_{j}; P_{j \in M.N}) \\ & \triangleright \{k_{(1,2)} : T_{(1,2)}; ![T_{(1,2)}]^{*} \circ T'_{(1,2)}; ?[T'_{(1,2)}]^{*}, \dots, k_{(1,N)} : T_{(1,N)}; ![T_{(1,N)}]^{*} \circ T_{(1,N)}; ?[T_{(1,N)}]^{*}, \\ & k_{(i,i+1)} : T_{(i,i+1)}; ![T_{(i,i+1)}]^{*} \circ T'_{(i,i+1)}; ?[T'_{(i,i+1)}]^{*}, \dots, k_{(i,N)} : T_{(i,N)}; ![T_{(i,N)}]^{*} \circ T'_{(i,N)}; ?[T'_{(i,N)}]^{*} \} \\ & \Gamma \vdash (Q_{1}; P_{1} \mid Q_{i}; P_{i \in 2...M-1} \mid Q_{j}; P_{j \in M..N}) \\ & \triangleright \{k_{(1,2)} : \bot, \dots, k_{(1,N)} : \bot, k_{(i,i+1)} : \bot, \dots, k_{(i,N)} : \bot \} \\ & \text{Case } E[e] \longrightarrow E[\texttt{false}] \\ & \text{By [ow2],} \\ & (v\tilde{k}) (\langle k_{(1,2)}, \dots, k_{(1,N)} \rangle.\texttt{outwhile}(e) \{Q_{1}[k_{(1,2)}, \dots, k_{(1,N)}]\} \\ & \mid \langle k_{(i,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)}, k_{(1,i)}, \dots, k_{(i-1,i)}]\} \text{ when } i \in \{2...M-1\} \\ & \mid \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle.\texttt{inwhile}\{Q_{i}[k_{(1,j)}, \dots, k_{(j-1,j)}]\}) \text{ when } j \in \{M..N\} \\ & \longrightarrow^{*} (v\tilde{k}) ( \begin{array}{c} \mathbf{0} \mid k_{(1,2)} \dagger \texttt{false} \mid \dots \mid k_{(1,N)} \dagger \texttt{false} \\ & \mid \langle k_{(1,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}(\langle k_{(1,j)}, \dots, k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}(\langle k_{(1,i)}, \dots, k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_{i}[k_{(i,i+1)}, \dots, k_{(i,N)} \rangle.\texttt{outwhile}\{Q_{i}[k_{(1,j)}, \dots, k_{(i-1,i)}]\} \text{ when } i \in \{2...M-1\} \\ & \mid \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle.\texttt{inwhile}\{Q_{i}[k_{(1,j)}, \dots, k_{(j-1,j)}]\} \text{ when } i \in \{2...M-1\} \\ & \mid \langle k_{(1,j)}, \dots, k_{(j-1,j)} \rangle.\texttt{inwhile}\{Q_{i}[k_{(1,j)}, \dots, k_{(j-1,j)}]\} \text{ when } i \in \{M..N\} \end{cases}
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\begin{split} \Gamma &\vdash (\ \mathbf{0} \ | \ P_{i \in 2..M-1} \ | \ P_{j \in M..N} \ | \ k_{(1,2)} \dagger [\texttt{false}] \ | \ \dots \ | \ k_{(1,N)} \dagger [\texttt{false}]) \\ & \triangleright \{k_{(1,2)} \colon \tau.\texttt{end} \circ ?[T_{(1,2)}]^{*\dagger}, \dots, k_{(1,N)} \colon \tau.\texttt{end} \circ ?[T_{(1,N)}]^{*\dagger}, \\ & k_{(i,i+1)} \colon ![T_{(i,i+1)}]^* \circ ?[T'_{(i,i+1)}]^*, \dots, k_{(i,N)} \colon ![T_{(i,N)}]^* \circ ?[T'_{(i,N)}]^* \} \end{split}
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By [IwE2],

$$\Gamma \vdash (\mathbf{0} \mid P_{i \in 2..M-1} \mid P_{j \in M..N})$$

$$\triangleright \{k_{(1,2)} : \tau.\mathsf{end} \circ \tau.\mathsf{end}, k_{(1,3)} : \tau.\mathsf{end} \circ ?[T'_{(1,3)}]^{*\dagger} \dots, k_{(1,N)} : \tau.\mathsf{end} \circ ?[T'_{(1,N)}]^{*\dagger}$$

$$k_{(i,i+1)} : ![T_{(i,i+1)}]^* \circ ?[T'_{(i,i+1)}]^*, \dots, k_{(i,N)} : ![T_{(i,N)}]^* \circ ?[T'_{(i,N)}]^* \}$$

By [Ow2],

$$\begin{split} & | \ \overline{\langle k_{(i,i+1)},\ldots,k_{(i,N)} \rangle}.\texttt{outwhile}(\langle k_{(1,i)},\ldots,k_{(i-1,i)} \rangle.\texttt{inwhile}) \{ \\ & Q_i[k_{(i,i+1)},\ldots,k_{(i,N)},k_{(1,i)},\ldots,k_{(i-1,i)}] \} \text{ when } i \in \{3..M-1\} \\ & | \ \langle k_{(1,j)},\ldots,k_{(j-1,j)} \rangle.\texttt{inwhile}\{ Q_j[k_{(1,j)},\ldots,k_{(j-1,j)}] \}) \text{ when } j \in \{M..N\} \end{split}$$

$$\begin{split} \Gamma &\vdash (\mathbf{0} \mid k_{(2,3)} \dagger [\texttt{false}] \mid \dots \mid k_{(2,N)} \dagger [\texttt{false}] \mid k_{(1,3)} \dagger [\texttt{false}] \mid \dots \mid k_{(1,N)} \dagger [\texttt{false}] \\ &\mid \mathbf{0} \mid P_i \text{ when } i \in \{3..M-1\} \mid P_j \text{ when } j \in \{M..N\}) \\ &\triangleright \{k_{(1,2)} \colon \tau.\texttt{end}, k_{(1,3)} \colon \tau.\texttt{end} \circ ?[T'_{(1,3)}]^{*\dagger} \dots, k_{(1,N)} \colon \tau.\texttt{end} \circ ?[T'_{(1,N)}]^{*\dagger} \\ &\quad k_{(2,3)} \colon \tau.\texttt{end} \circ ?[T'_{(2,3)}]^{*\dagger}, \dots, k_{(2,N)} \colon \tau.\texttt{end} \circ ?[T'_{(2,N)}]^{*\dagger}, \\ &\quad k_{(i,i+1)} \colon ![T_{(i,i+1)}]^* \circ ?[T'_{(i,i+1)}]^*, \dots, k_{(i,N)} \colon ![T_{(i,N)}]^* \circ ?[T'_{(i,N)}]^* \} \end{split}$$

By repeatedly apply [Ow2] and [IwE2] as above on processes $P_3..P_{M-1}$

$$\begin{split} & \Gamma \vdash (\ \mathbf{0} \ | \ \mathbf{0} \ \text{when} \ i \in \{2..M-1\} \ | \ k_{(1,j)} \dagger [\texttt{false}] \ | \ \dots \ | \ k_{(j-1,j)} \dagger [\texttt{false}] \ | \ P_{j \in M..N}) \\ & \triangleright \{k_{(1,2)} \colon \tau.\texttt{end}, \dots, k_{(1,N)} \colon \tau.\texttt{end}, k_{(i,i+1)} \colon \tau.\texttt{end}, \dots, k_{(i,M-1)} \colon \tau.\texttt{end} \circ ?[T'_{(i,M-1)}]^*, \\ & \quad k_{(1,j)} \colon \tau.\texttt{end} \circ ?[T'_{(1,j)}]^{*\dagger}, \dots, k_{(j-1,j)} \colon \tau.\texttt{end} \circ ?[T'_{j-1,j}]^{*\dagger} \} \end{split}$$

Finally apply [Iw2],

-

$$\begin{split} & \Gamma \vdash (\mathbf{0} \mid \mathbf{0} \text{ when } i \in \{2..M-1\} \mid \mathbf{0} \text{ when } j \in \{M..N\}) \\ & \triangleright \{k_{(1,2)} \colon \tau.\text{end}, \dots, k_{(1,N)} \colon \tau.\text{end}, k_{(i,i+1)} \colon \tau.\text{end}, \dots, k_{(i,N)} \colon \tau.\text{end}\} \end{split}$$

Finally, apply [BOT].

For other cases, the proof is similar to [16, P. 56-60]

Theorem 4.3 (Deadlock freedom) Assume *P* forms a well-formed topology and $\Gamma \vdash P \triangleright \Delta$. Then *P* is deadlock-free.

Proof. Assume $\Gamma \vdash \Pi_i P_i \triangleright \vec{k} : \vec{\perp}$ for all cases below. Suppose group of *n* parallel composed processes $\Pi_i P_i = P_1 \mid \ldots \mid P_n$ conforms to a well-formed topology (Definition 4.1).

Case 1.1 inwhile and outwhile, condition true.

 $\begin{array}{l} P_1 = \langle \vec{k}_1 \rangle . \texttt{outwhile}(\texttt{true}) \{ Q_1 \} \ P_i = \langle \vec{k}_i \rangle . \texttt{outwhile}(\langle \vec{k}_i' \rangle . \texttt{inwhile}) \{ Q_i \} \ (2 \leq i < n) \\ P_n = \langle \vec{k}_n' \rangle . \texttt{inwhile}\{ Q_n \} \qquad \vec{k}_i \subset k_{(i,i+1)} \cdots k_{(i,n)}, \vec{k}_i' \subset k_{(1,i)} \cdots k_{(i-1,i)} \end{array}$

If the outwhile condition is true, the iteration chain passes the true condition from the Master process, P_1 to all other processes, at the end of the iteration chain, all processes reduce to Q_i ; P_i where Q_i is the loop body and P_i is the next iteration of the outwhile/inwhile loop. We will show inductively that Q_i is deadlock free in other cases listed below. The session channel interaction sequence is shown below.

$$P_{1} = \xrightarrow{k_{(1,2)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(i,i+1)}}^{*} \xrightarrow{Q_{1}; P_{1}} Q_{1}; P_{1}$$

$$P_{i} = \xrightarrow{k'_{(1,i)}}^{*} \xrightarrow{k'_{(1,i)}}^{*} \xrightarrow{k'_{(1,i)}}^{*} \xrightarrow{k'_{(i,i)}}^{*} \xrightarrow{Q_{1}; P_{1}} \xrightarrow{k'_{(i,i)}}^{*} Q_{i}; P_{i}$$

$$P_{n} = \xrightarrow{k'_{(1,i)}}^{*} \xrightarrow{$$

The Master process P_1 initiates the interactions in each outwhile/inwhile iteration chain. All session interactions in each process happen after all interactions on the left is completed. From above interaction sequence, there are no processes that can only proceed depending on an interaction step not readily available. Therefore a correct process P_i will always reduce to Q_i ; P_i for a true condition.

Case 1.2 inwhile and outwhile, condition false. $P_1 = \langle \vec{k}_1 \rangle . \text{outwhile}(\texttt{false}) \{Q_1\} P_i = \langle \vec{k}_i \rangle . \text{outwhile}(\langle \vec{k}'_i \rangle . \texttt{inwhile}) \{Q_i\} (2 \le i < n)$ $P_n = \langle \vec{k}'_n \rangle . \texttt{inwhile} \{Q_n\} \qquad \vec{k}_i \subset k_{(i,i+1)} \cdots k_{(i,n)}, \vec{k}'_i \subset k_{(1,i)} \cdots k_{(i-1,i)}$ Suppose group of parallel composed processes $\Pi_i P_i = P_1 \mid \ldots \mid P_n$ conforms to a

Suppose group of parallel composed processes $\prod_i P_i = P_1 \mid ... \mid P_n$ conforms to a well-formed topology (Definition 4.1). If the outwhile condition is false, the iteration chain passes the false condition from the Master process, P_1 to all other processes, at the end of the iteration chain, all processes reduces to **0** to exit from the outwhile/in-while loop. The session channel interaction sequence is shown below:

$$P_{1} = \xrightarrow{k_{(1,2)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,n)}}^{*} \xrightarrow{k_{(1,n)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(i,i+1)}}^{*} \xrightarrow{k_{(i,i+1)}}^{*} \xrightarrow{k_{(i,n)}}^{*} \underbrace{k_{(i,n)}}^{*} \underbrace{0}_{k_{(i,n)}}^{*} \underbrace{k_{(i,n)}}^{*} \underbrace{0}_{k_{(i,n)}}^{*} \underbrace{k_{(i,n)}}^{*} \underbrace{0}_{k_{(i,n)}}^{*} \underbrace$$

The interaction sequence is same as the first case, therefore all processes P_i can reduce to **0** with similar reasoning.

Case 2. sequencing. Suppose for a simple case $P_i = Q_{1i}; Q_{2i}$, and both $\Pi_i Q_{1i}$ and $\Pi_i Q_{2i}$ are deadlock free. By Definition C.1, sequential composition will not permute the order of communication of each of the processes. Therefore $\Pi_i P_i$ is deadlock free.

We can show that $\Pi_i P_i$ with $P_i = Q_{i1}; Q_{i2}; ...; Q_{in}$ is deadlock free if all the subprocesses are deadlock free by induction.

Case 3. base. This case considers a group of processes P_i which do not include shared session channels, inwhile nor outwhile. The session actions in each of P_i follows the order of the index, similar to Case 1.1 and Case 1.2. The session channel interaction sequence is shown below:

$$P_{1} = \xrightarrow{k_{(1,2)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(i,i+1)}}^{*} \xrightarrow{P_{1}'} P_{1}'$$

$$P_{i} = \xrightarrow{k_{(1,i)}'}^{*} \xrightarrow{k_{(i,i)}}^{*} \xrightarrow{k_{(i,i+1)}'}^{*} \xrightarrow{k_{(i,i+1)}}^{*} \xrightarrow{k_{(i,i)}}^{*} P_{i}'$$

$$P_{n} = \xrightarrow{k_{(1,i)}'}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} \xrightarrow{k_{(1,i)}}^{*} P_{n}'$$

The body of the process is deadlock free with same reasoning in Case 1.1, which then reduces to a *base* process P'_i , and $\Pi_i P'_i$ reduces to **0** by the reason described in § 4.2.